

[illegible]

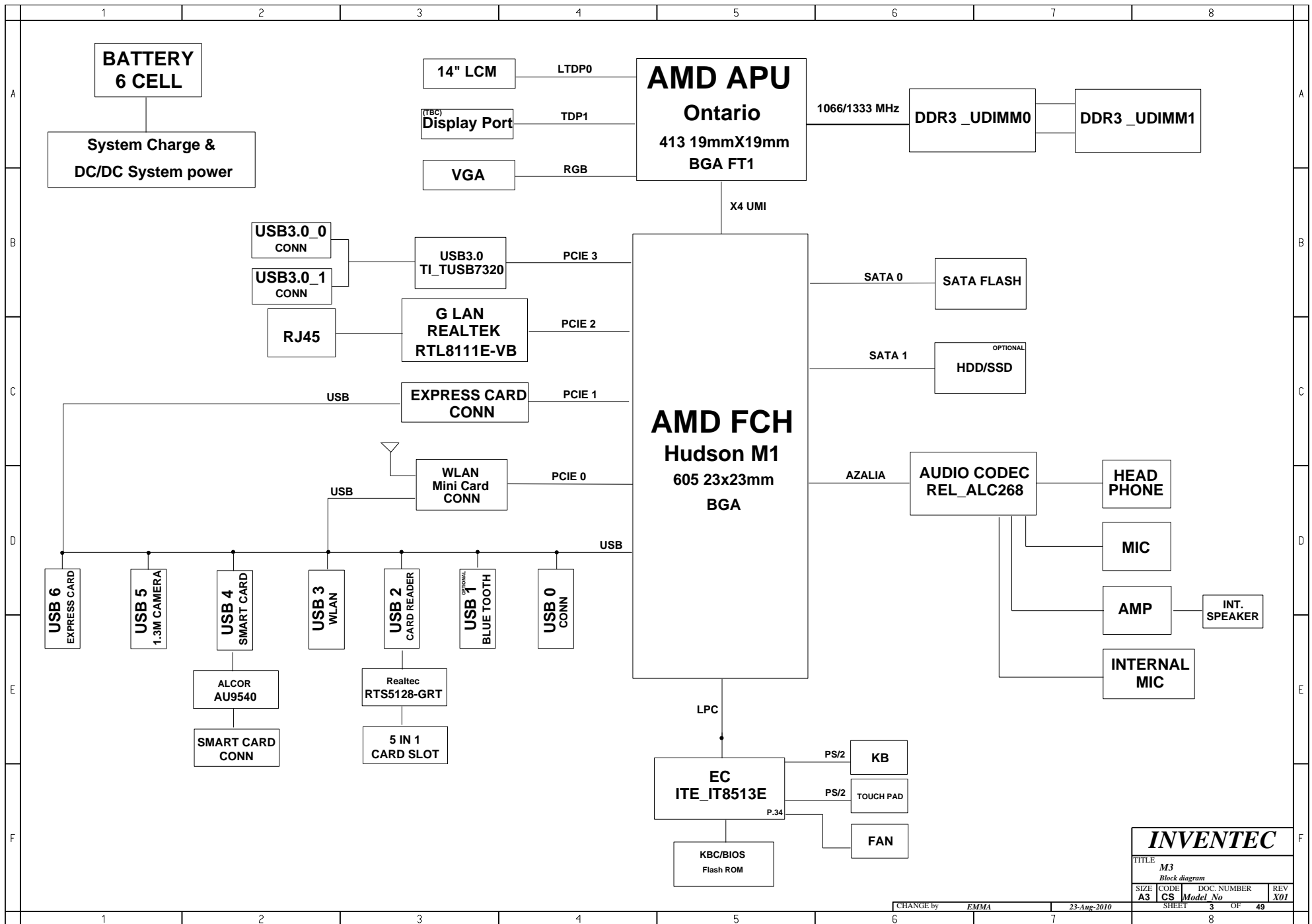
TABLE OF CONTENTS

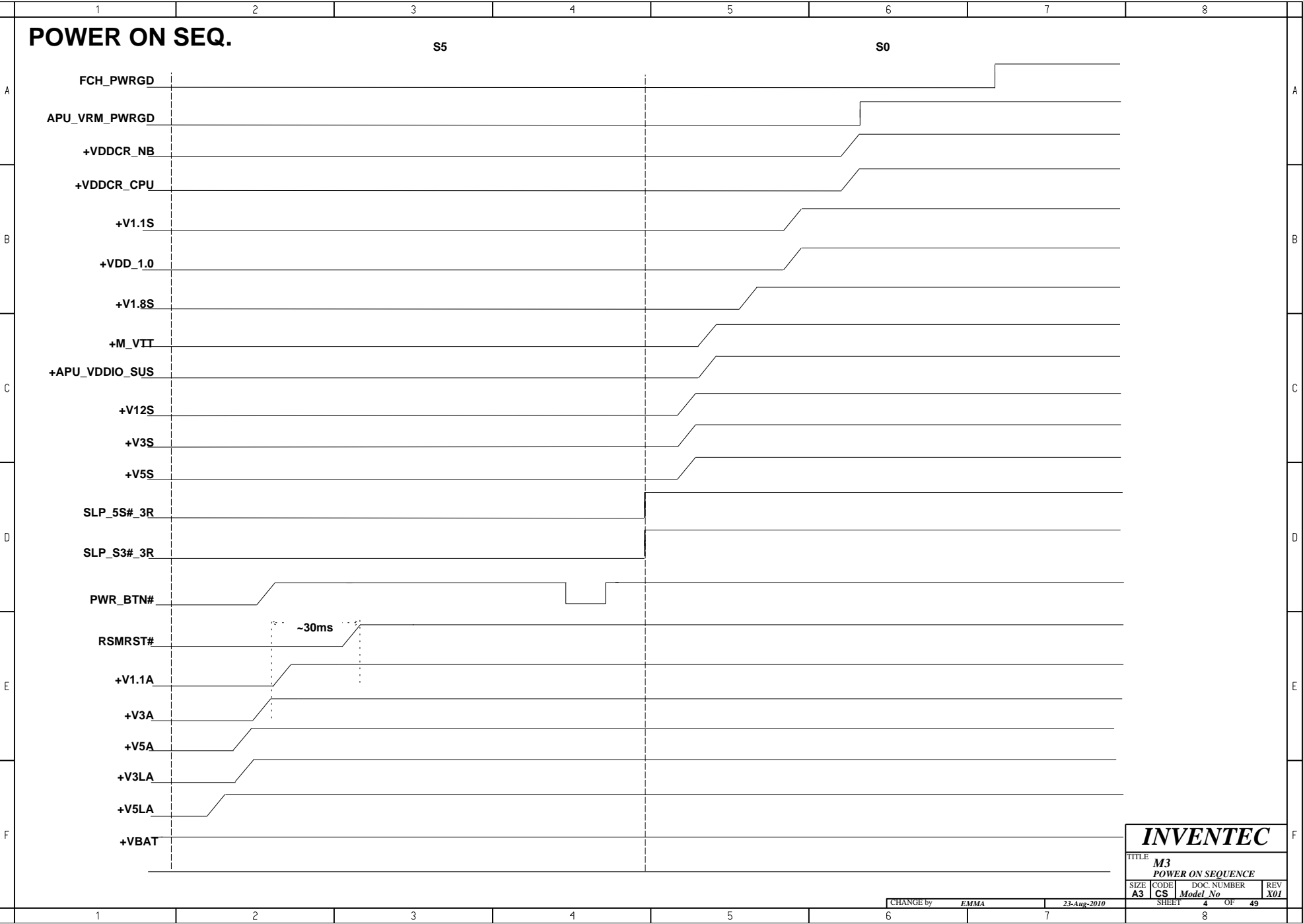
PAGE	PAGE
1. COVER PAGE	28.SATA FLASH CONN
2. INDEX	29.CODEC REL_ALC268
3. BLOCK DIAGRAM	30.AMP / INT. SPEAKER
4. POWER ON SEQUENCE	31.SMART CARD AU9540
5-13. RESERVE FOR SYSTEM POWER	32.GIGA LAN
14.APU FT1 1	33.RJ45 / DUAL USB
15.APU FT1 2	34.IT8712
16.APU FT1 3	35.SERIAL / PARALLEL CONN
17.APU FT1 4	36.PS/2 CONN
18.DDR SO-DIMM	37.USB3.0 CONTROLLER
19.FCH1	38.DUAL USB3.0 CONN / PIN HEADER
20.FCH2	39.USB CONN
21.FCH3	40.AUDIO JACK / POWER SW
22.FCH4	41.RISER CARD
23.STRAP OPTION / THERMAL / RTC	
24.DVI-I	
25.DVI-D	
26.WLAN / BT	
27.TPM	

INVENTEC

TITLE M3 <i>index</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
SHEET 2		OF 49	

CHANGE by **EMMA** 23-Aug-2010





INVENTEC

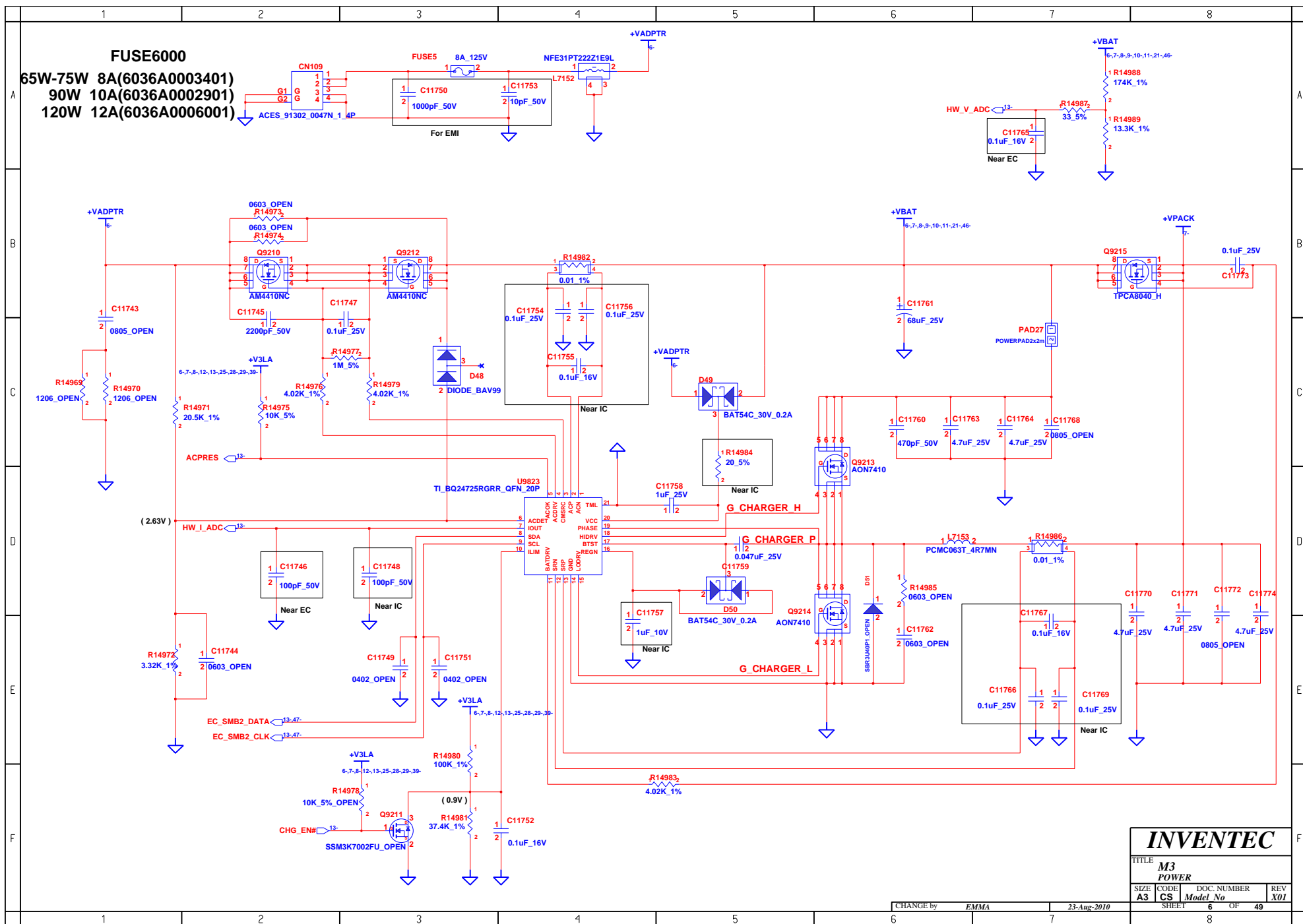
TITLE
M3
POWER ON SEQUENCE
SIZE CODE DOC. NUMBER REV
A3 CS Model No X01
SHEET 4 OF 49

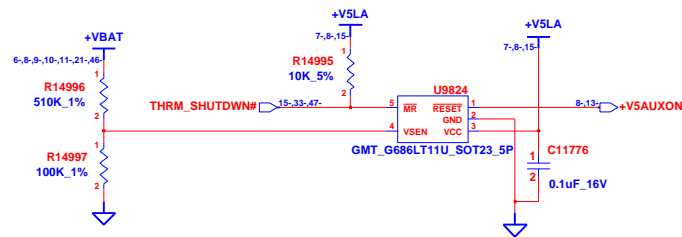
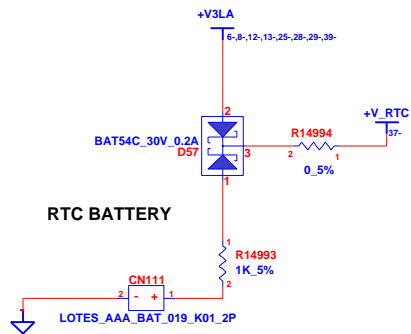
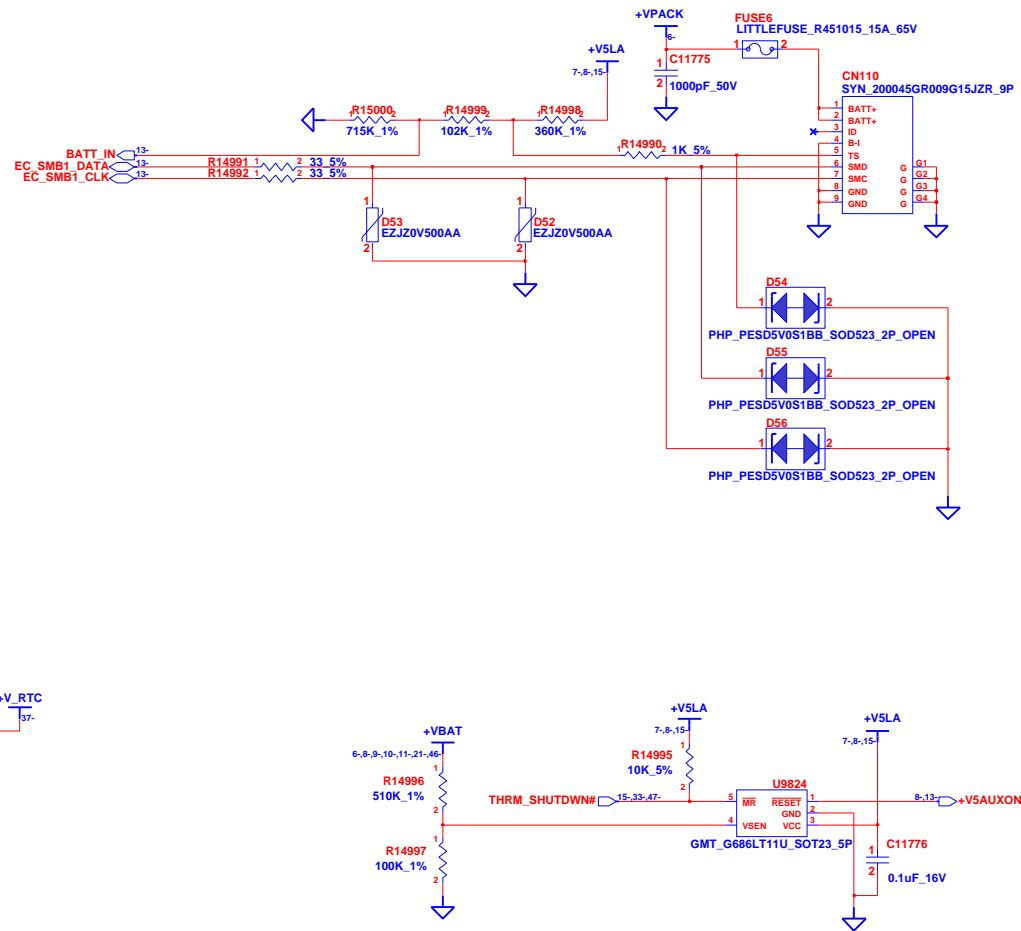
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A	<div>Power Block Diagram</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC

TITLE			
M3 POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		5	OF 49

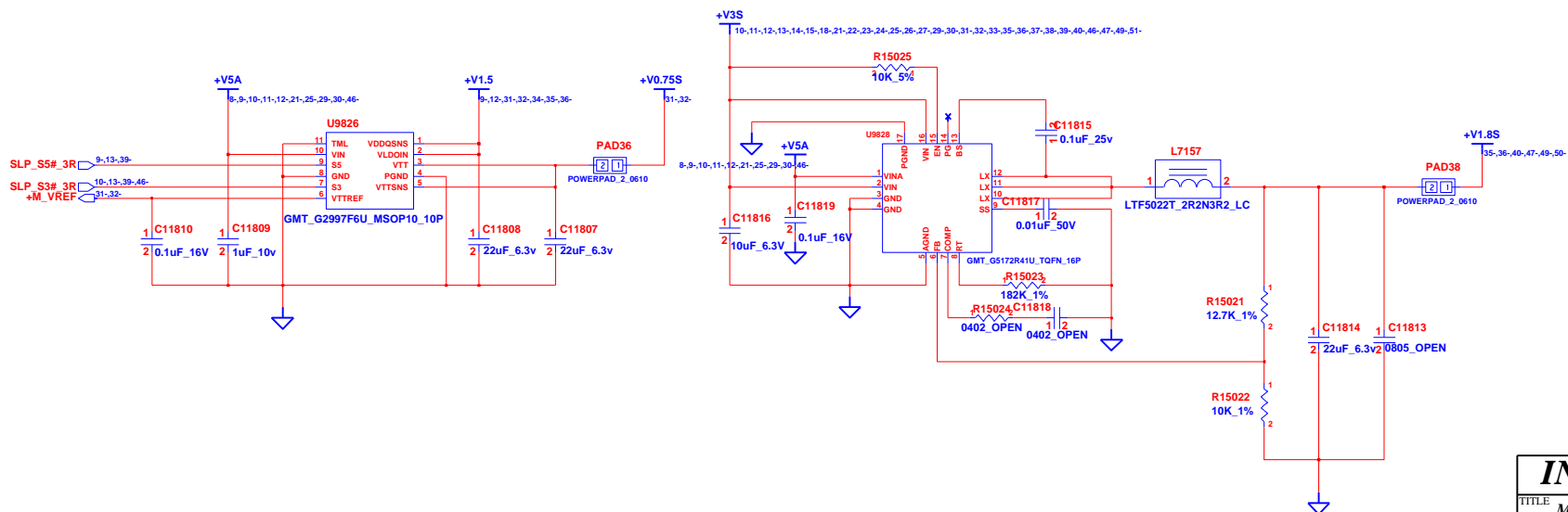
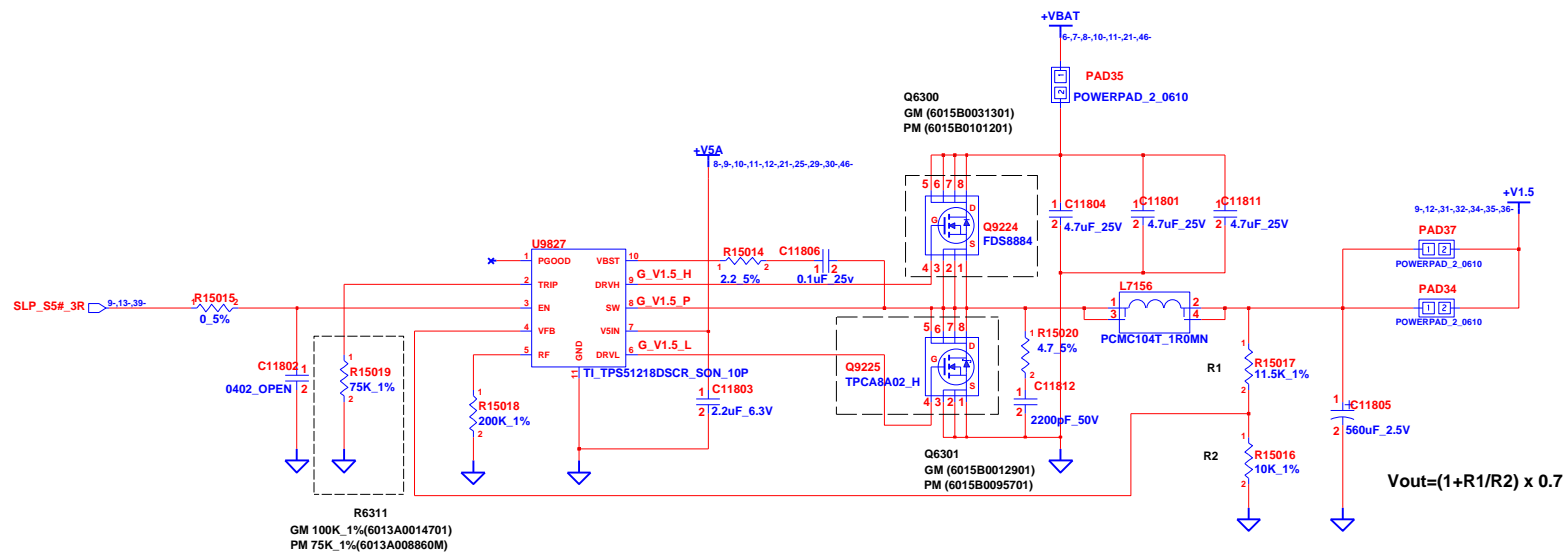
CHANGE by EMMA 23-Aug-2010





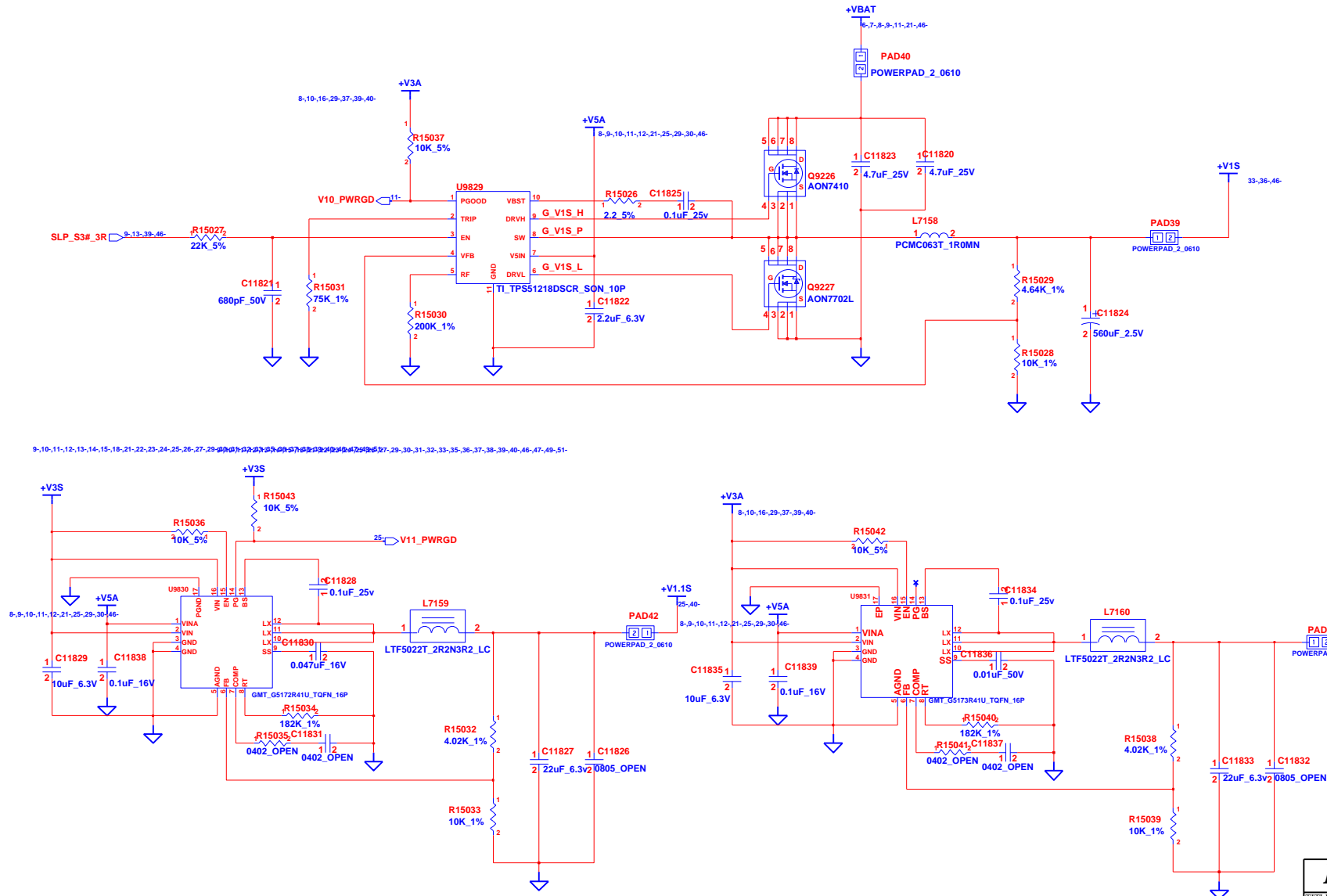
INVENTEC			
TITLE			
M3			
POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		7	OF 49

CHANGE by EMMA 23-Aug-2010



TITLE			
M3 POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

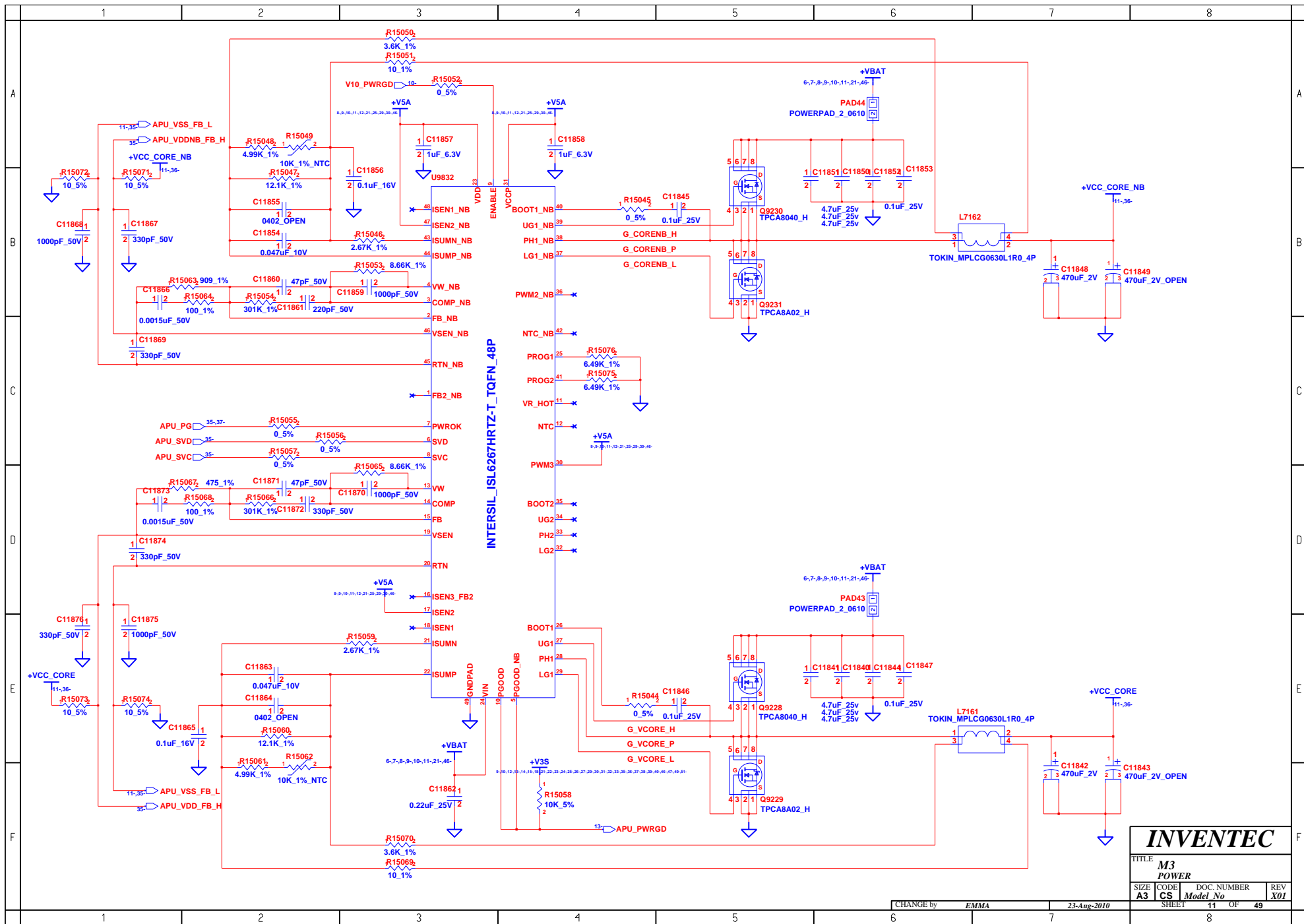
+V1.1A +V1S +V1.1S



INVENTEC

TITLE			
M3			
POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		10	OF 49

CHANGE by EMMA 23-Aug-2010

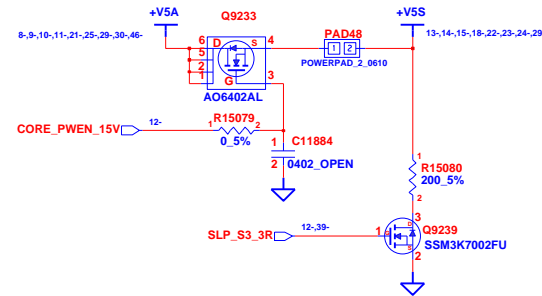
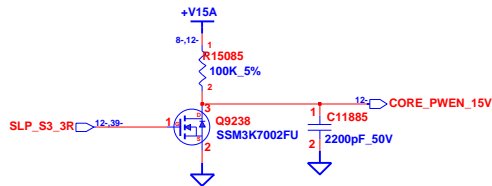
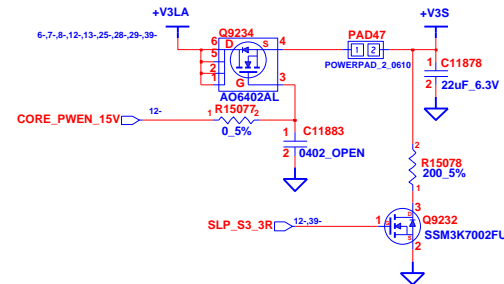
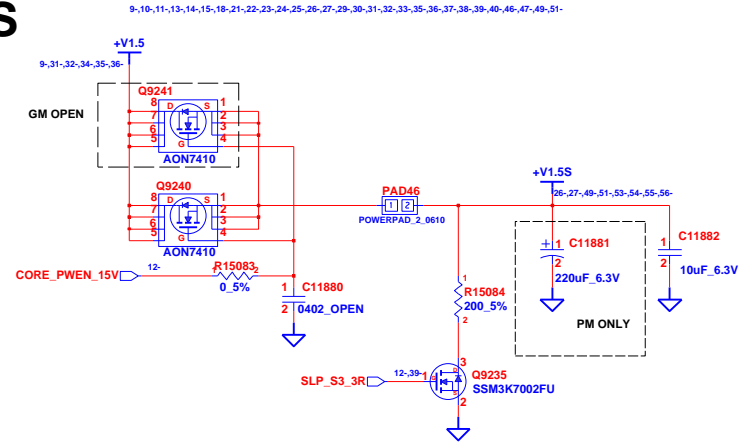
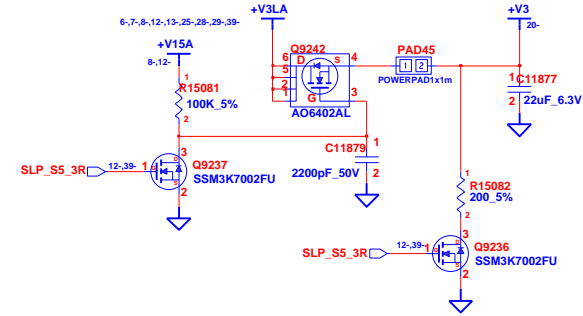


INVENTEC

TITLE			
M3 POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		11 OF	49

CHANGE by EMMA 23-Aug-2010

+V3 +V5S +V3S +V1.5S



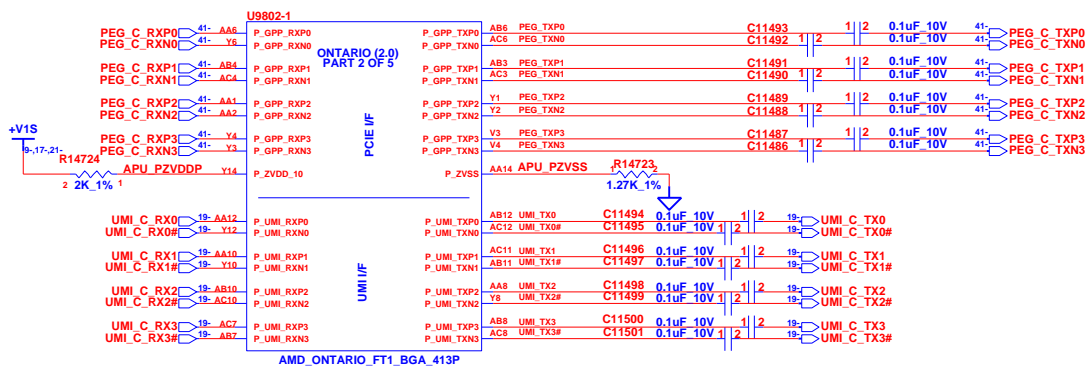
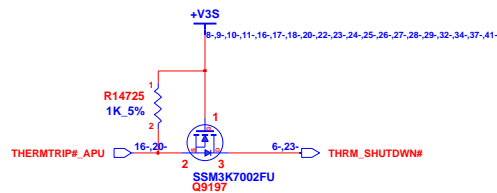
INVENTEC

TITLE			
M3			
POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		12	OF 49

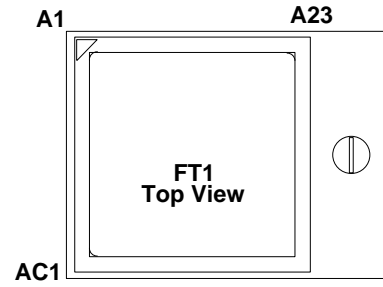
CHANGE by EMMA 23-Aug-2010

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>EMMA</i>		23-Aug-2010	
1	2	3	4	5	6	7	8		

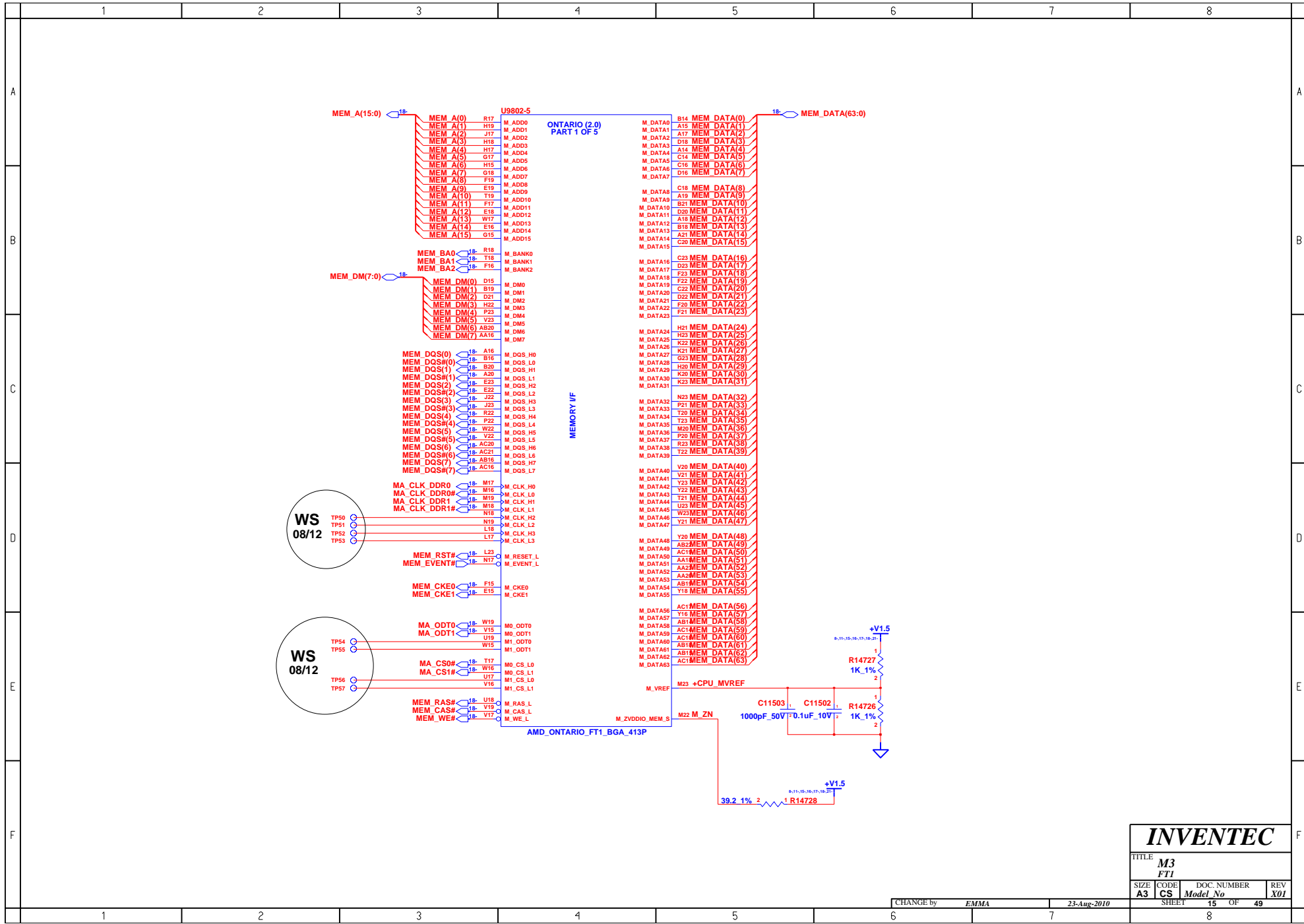
INVENTEC			
TITLE <i>M3 POWER</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>X01</i>
SHEET		13	OF 49



Layout: Add stitching caps if crossing plane split.



INVENTEC			
TITLE			
M3			
FT1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
CHANGE by		EMMA	23-Aug-2010
SHEET		14	OF 49



INVENTEC

TITLE
**M3
FT1**

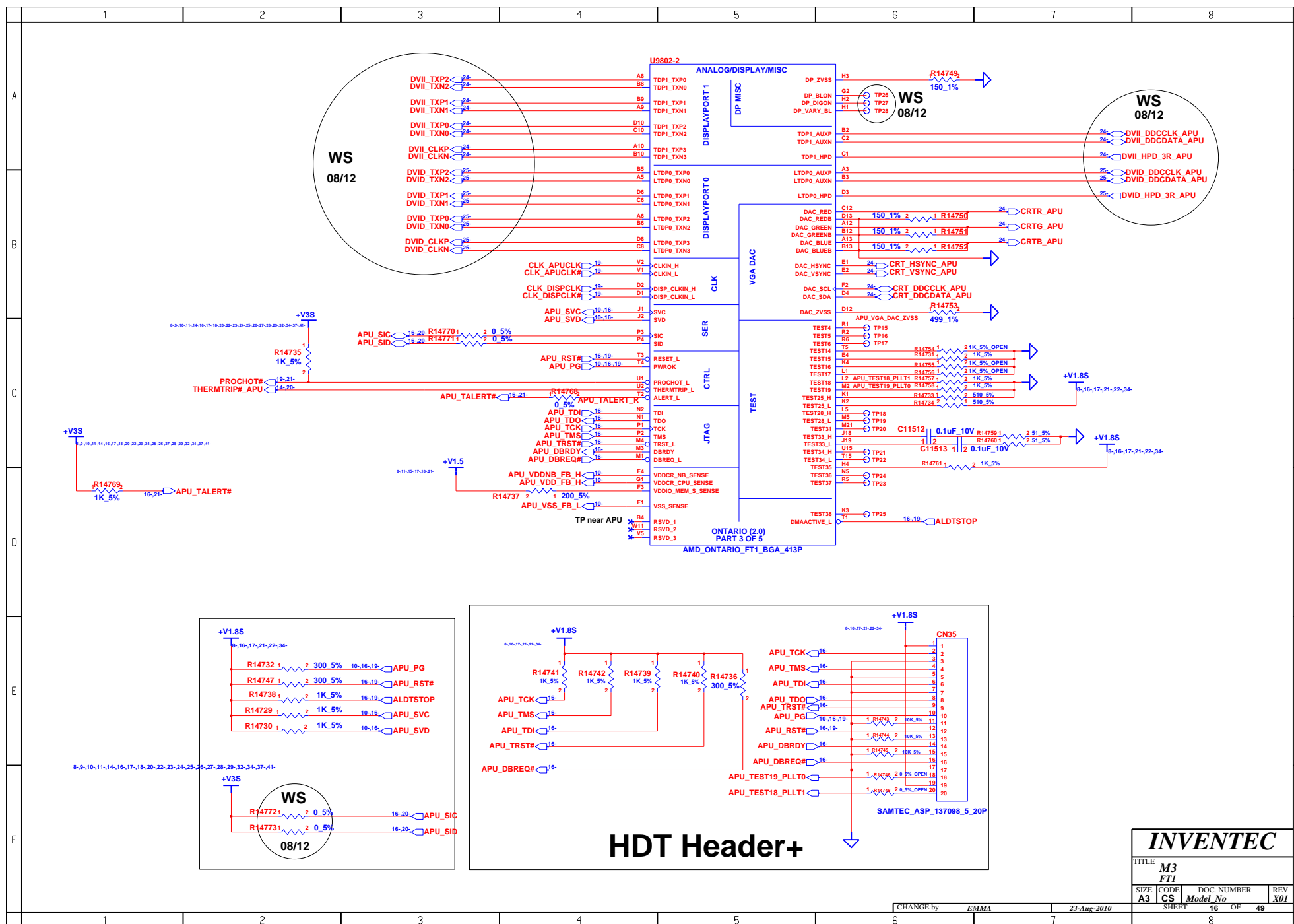
SIZE CODE
A3 CS

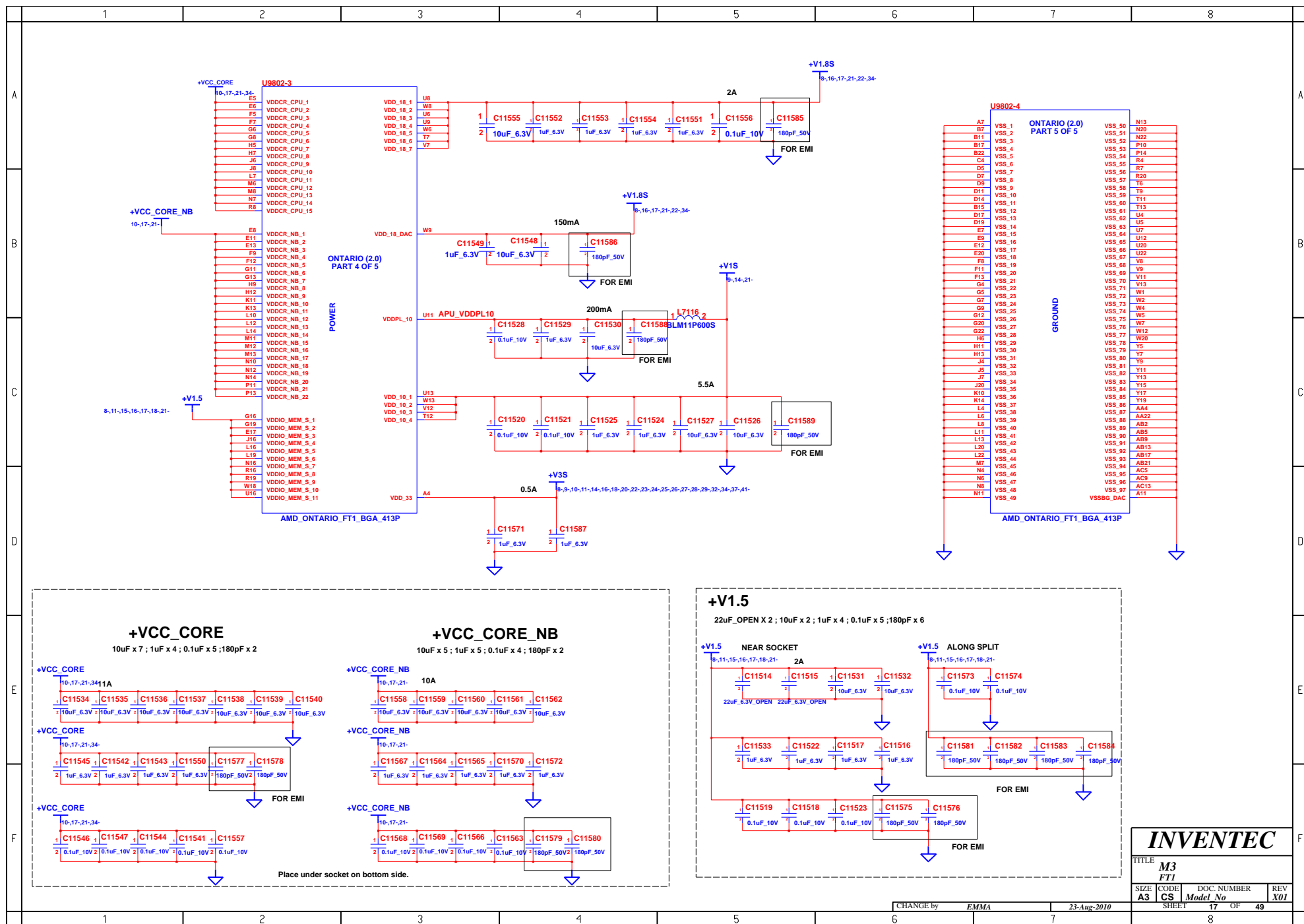
DOC. NUMBER
Model No

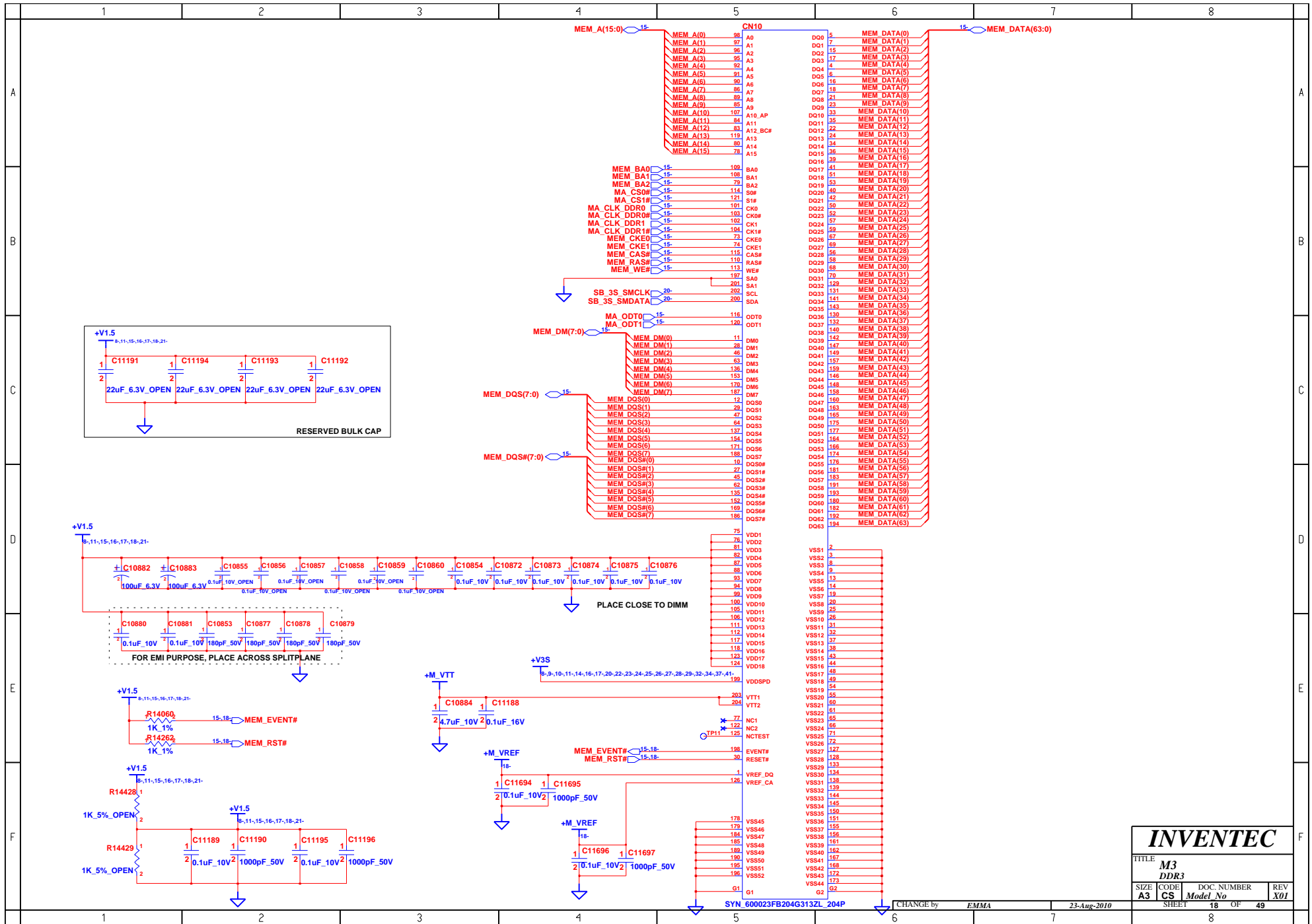
REV
X01

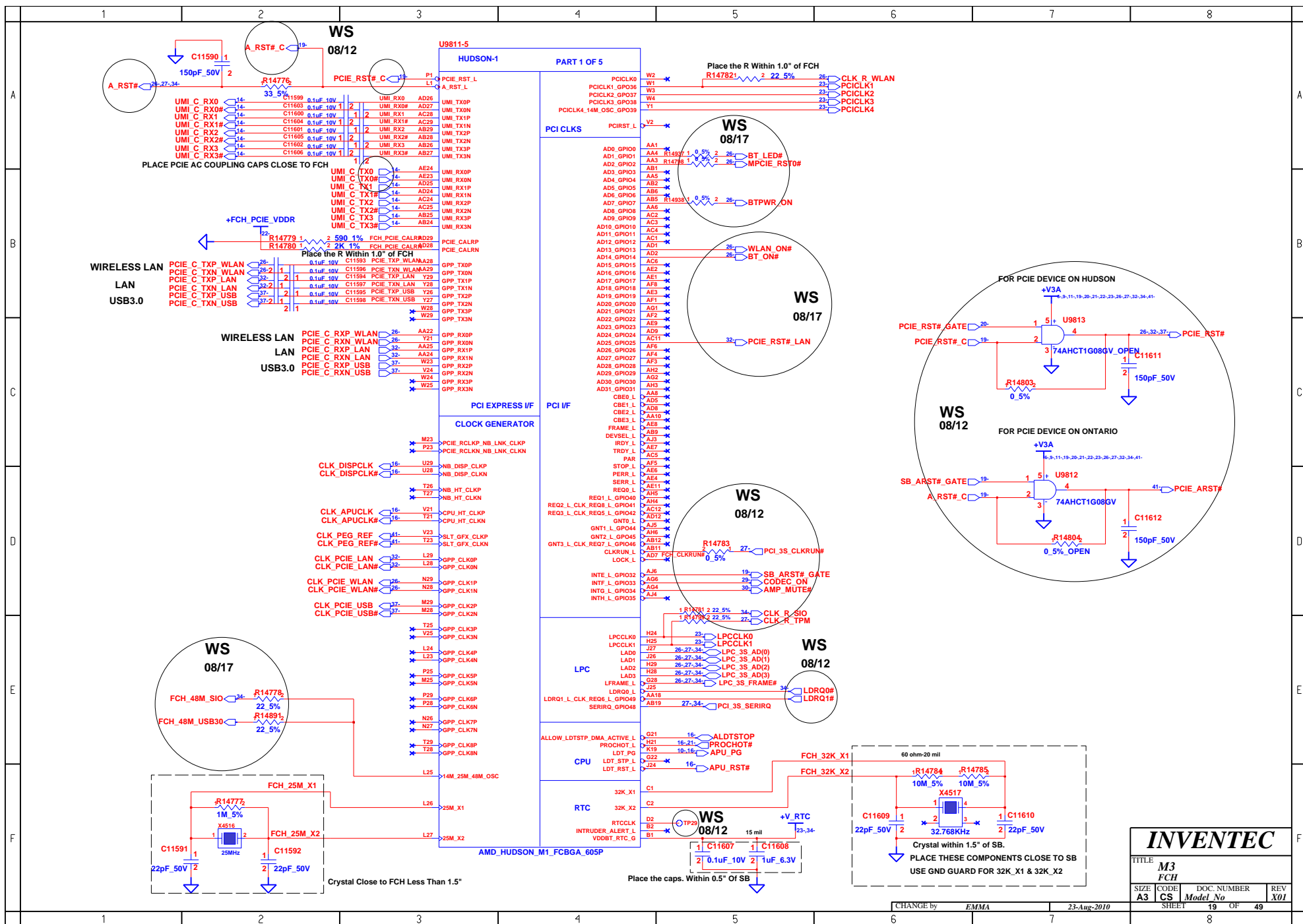
CHANGE by **EMMA** 23-Aug-2010

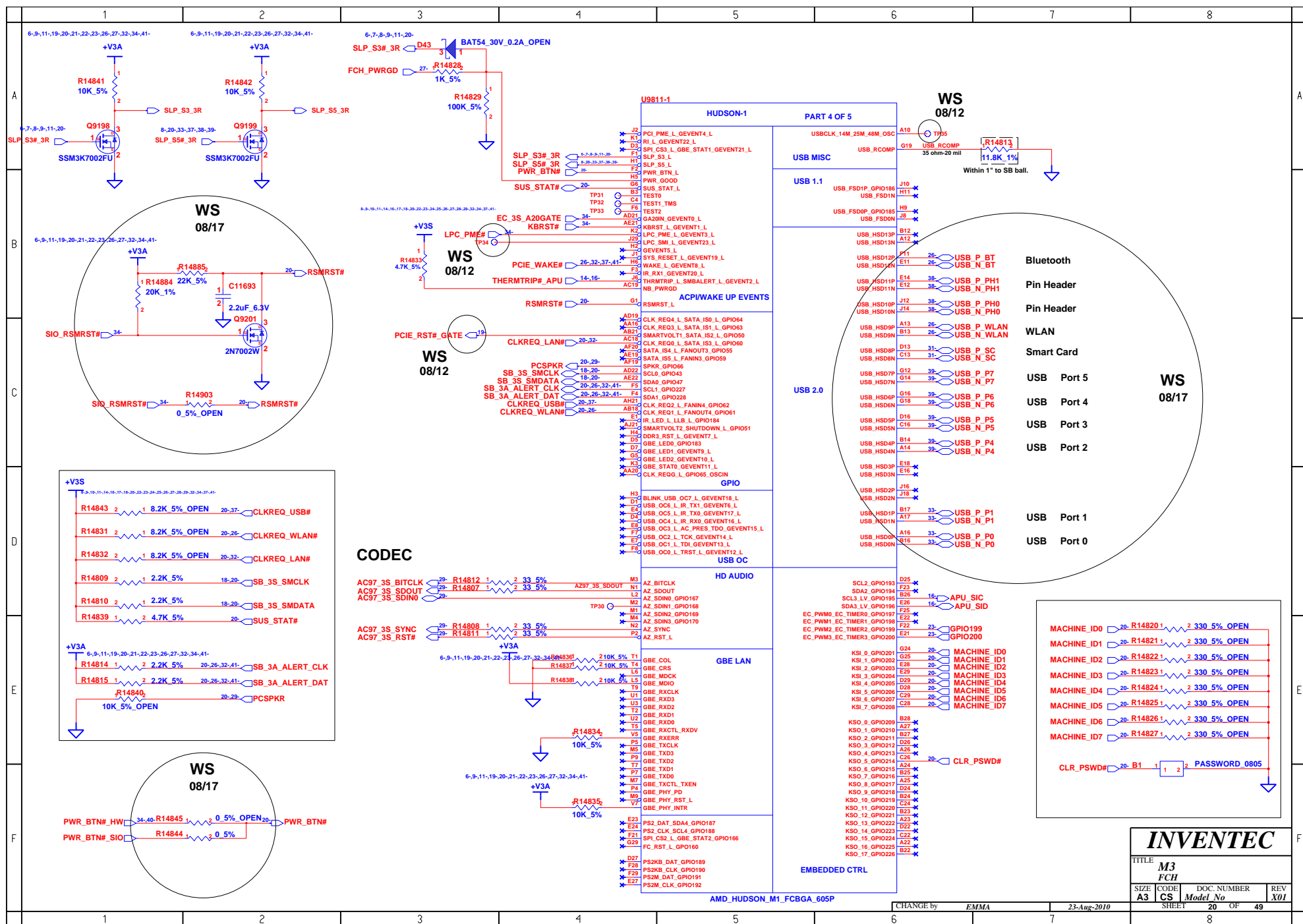
SHEET **15** OF **49**

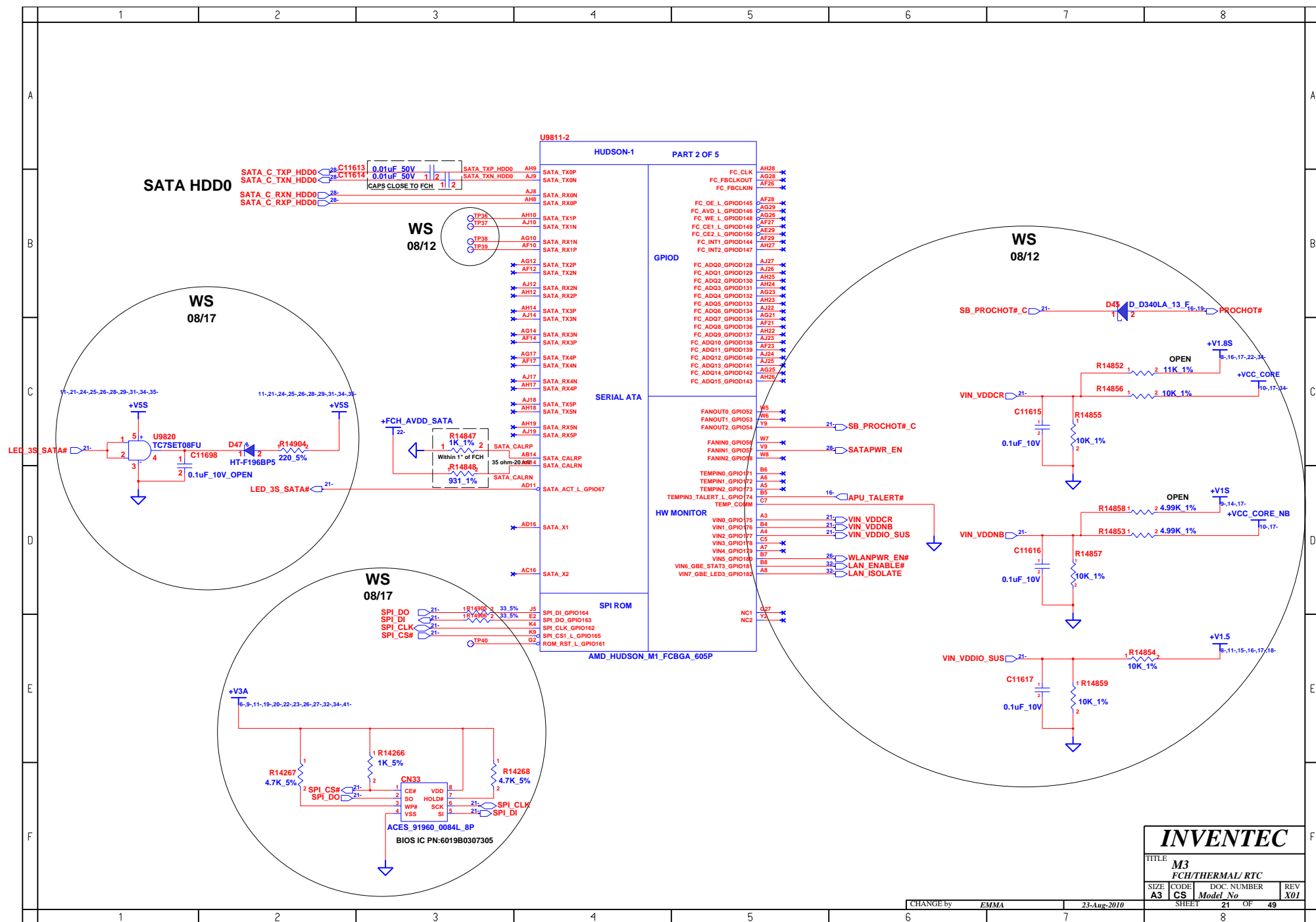


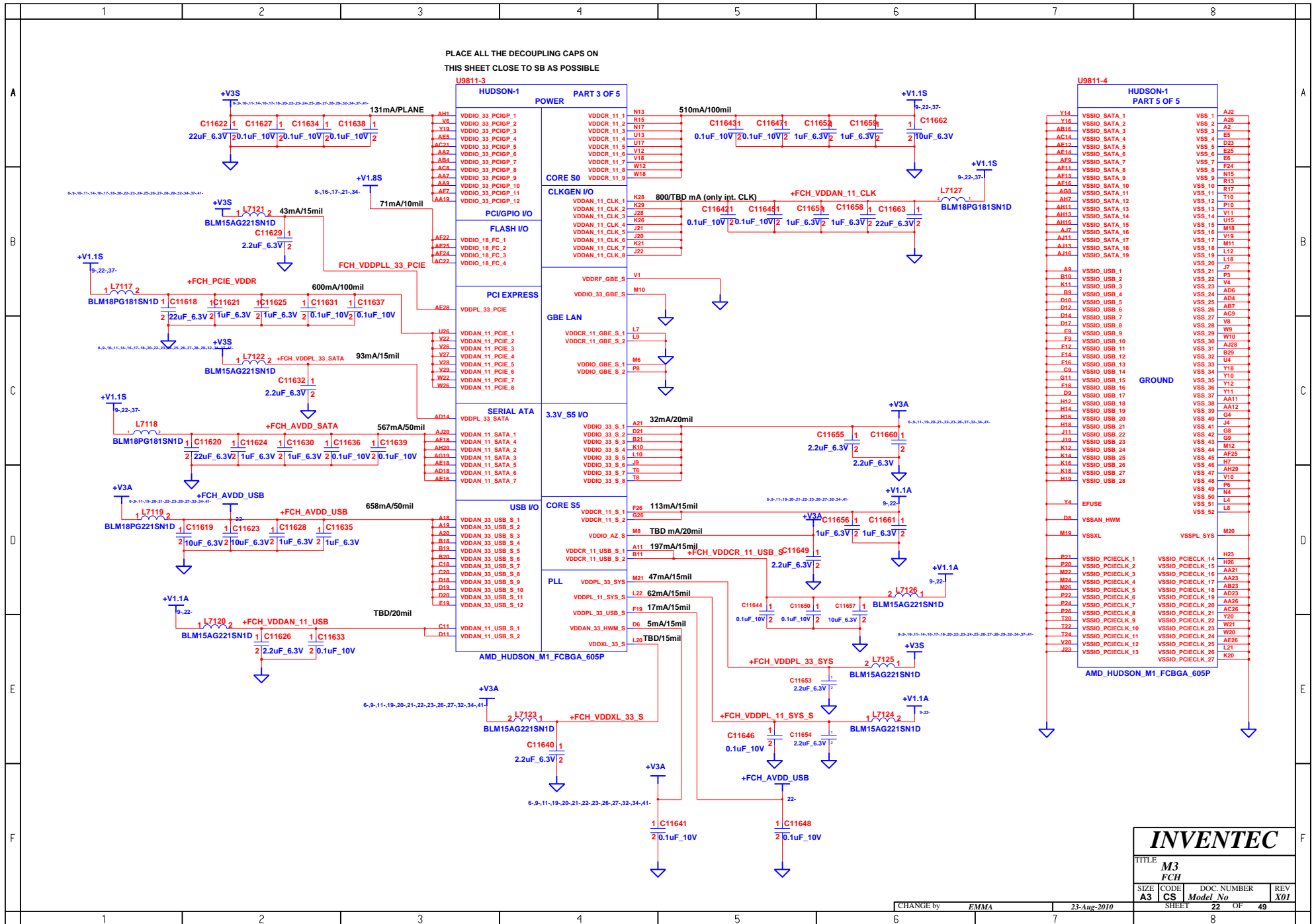




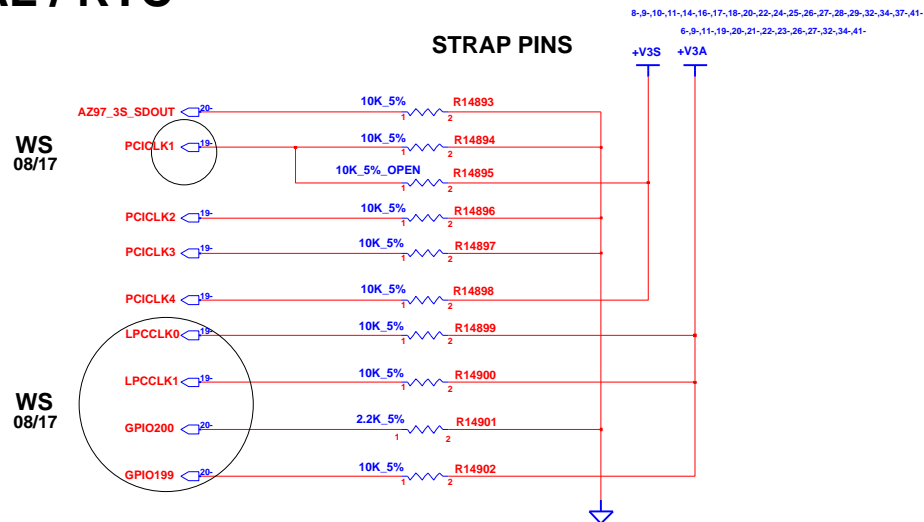






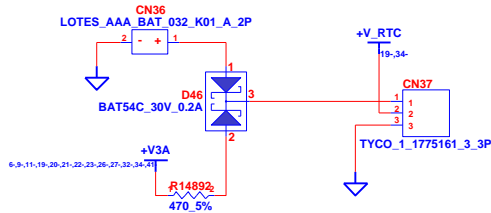


STRAP OPTION / THERMAL / RTC

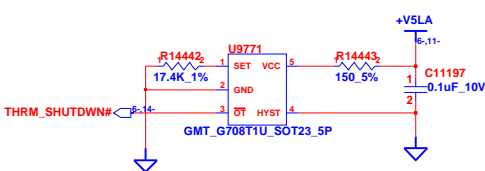


	AZ97_3S_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPCCLK0	LPCCLK1	GPIO200 EC_PWM3	GPIO199 EC_PWM2
PULL HIGH	Low Power Mode (Not Supported)	Allow PCIe Gen2	Watchdog Timer Enabled (Not Supported)	Debug STRAP Enabled	Non-FUSION CLOCK Mode	Integrated Micro-Controle Enabled (Default)	Internal Clock Generator Enabled (Default)	H,H= Reserved H,L= SPI ROM (Default)	
PULL LOW	Preformance Mode (Default)	Force PCIe Gen1 (Default)	Watchdog Timer Disabled (Default)	Debug STRAP Disabled (Default)	FUSION CLOCK Mode (Default)	Integrated Micro-Controle Disabled	Internal Clock Generator Disabled	L,H= LPC ROM L,L= Reserved	

RTC



Thermal Sensor



INVENTEC

TITLE			
M3			
THERMAL / RTC			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		23 OF	49

CHANGE by EMMA 23-Aug-2010

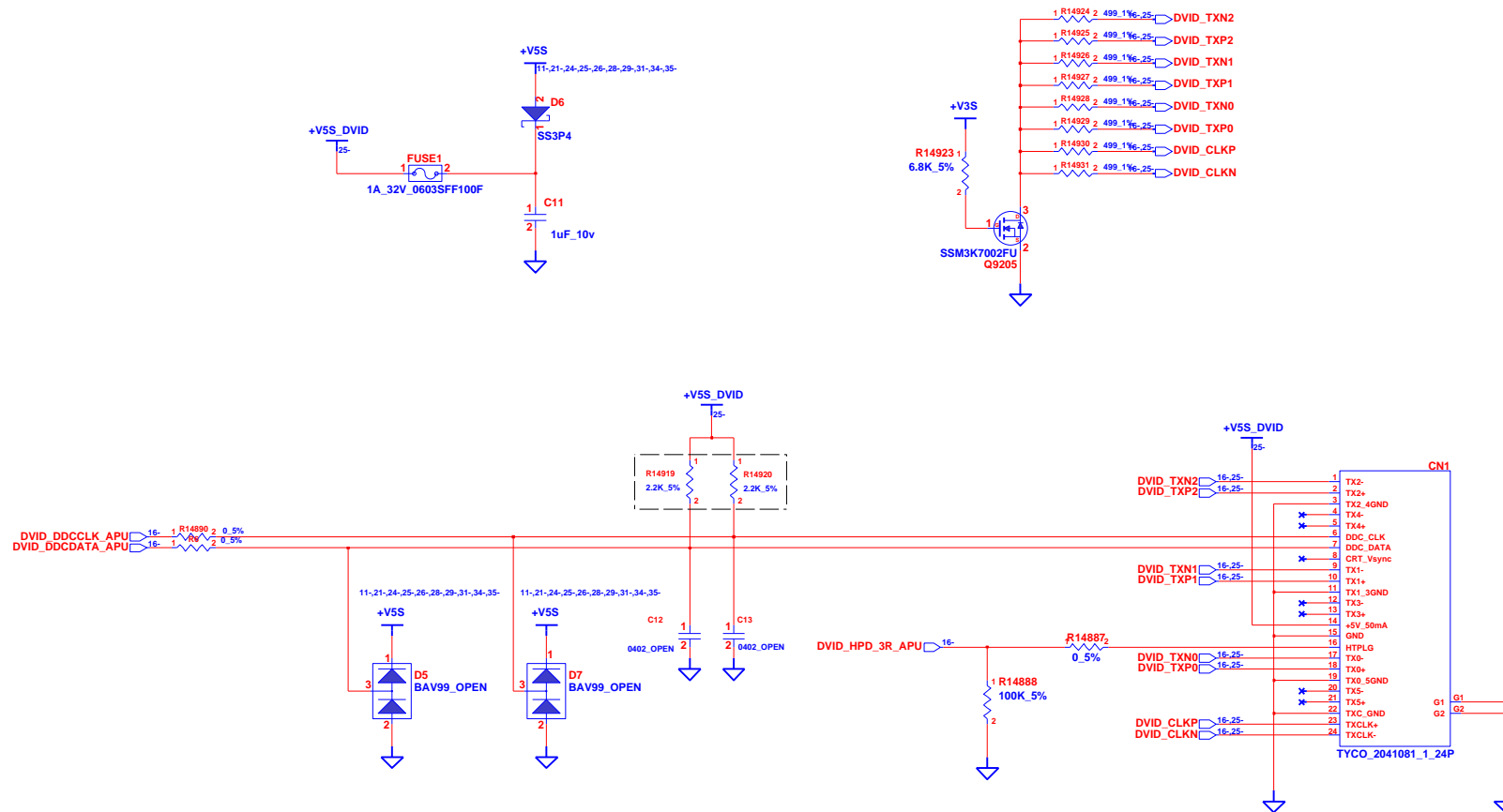
DVI-I

CRTR_APU, CRTG_APU, CRTB_APU, C11699, C11700, C11701, +VSS, CHENMKO_BAV99, +VSS_DVII, +VSS_SYNC, D29, U9742, C10768, HSYNC_R, VSYNC_R, R14027, R14028, HSYNC, VSYNC, R14909, SSM3K7002FU, Q9203, +V3S, R14811, R14812, R14813, R14814, R14815, R14816, R14817, R14818, DVII_TXN2, DVII_TXP2, DVII_TXN1, DVII_TXP1, DVII_TXN0, DVII_TXP0, DVII_CLKP, DVII_CLKN, +VSS, FUSE4, SS3P4, C11204, 1uF_10v, CRT_HSYNC_APU, R14024, 10K_5%, CRT_VSYNC_APU, R14025, 10K_5%, +VSS_DVII, +VSS_SYNC, R14478, R14479, 2.2K_5%, CRT_DDCCLK_APU, CRT_DDCDATA_APU, DVII_DDCCLK_APU, DVII_DDCDATA_APU, R14469, 0.5%, R14470, 0.5%, C11202, 0402_OPEN, C11203, 0402_OPEN, +VSS, D25, BAV99_OPEN, +VSS, D26, BAV99_OPEN, DVII_HPD_3R_APU, R14886, 0.5%, R14476, 100K_5%, +VSS_DVII, +VSS_SYNC, R14480, R14481, 2.2K_5%, DVII_TXN2, DVII_TXP2, CN9, TX2-, TX2+, TX2_4GND, TX4-, TX4+, DDC_CLK, DDC_DATA, CRT_Vsync, TX1-, TX1+, TX1_3GND, TX3-, TX3+, TX3_4GND, TX5-, TX5+, TX5_4GND, TX6-, TX6+, TX6_4GND, TX7-, TX7+, TX7_4GND, TX8-, TX8+, TX8_4GND, TX9-, TX9+, TX9_4GND, TX10-, TX10+, TX10_4GND, TX11-, TX11+, TX11_4GND, TX12-, TX12+, TX12_4GND, TX13-, TX13+, TX13_4GND, TX14-, TX14+, TX14_4GND, TX15-, TX15+, TX15_4GND, TX16-, TX16+, TX16_4GND, TX17-, TX17+, TX17_4GND, TX18-, TX18+, TX18_4GND, TX19-, TX19+, TX19_4GND, TX20-, TX20+, TX20_4GND, TX21-, TX21+, TX21_4GND, TX22-, TX22+, TX22_4GND, TX23-, TX23+, TX23_4GND, TX24-, TX24+, TX24_4GND, TX25-, TX25+, TX25_4GND, TX26-, TX26+, TX26_4GND, TX27-, TX27+, TX27_4GND, TX28-, TX28+, TX28_4GND, TX29-, TX29+, TX29_4GND, TX30-, TX30+, TX30_4GND, TX31-, TX31+, TX31_4GND, TX32-, TX32+, TX32_4GND, TX33-, TX33+, TX33_4GND, TX34-, TX34+, TX34_4GND, TX35-, TX35+, TX35_4GND, TX36-, TX36+, TX36_4GND, TX37-, TX37+, TX37_4GND, TX38-, TX38+, TX38_4GND, TX39-, TX39+, TX39_4GND, TX40-, TX40+, TX40_4GND, TX41-, TX41+, TX41_4GND, TX42-, TX42+, TX42_4GND, TX43-, TX43+, TX43_4GND, TX44-, TX44+, TX44_4GND, TX45-, TX45+, TX45_4GND, TX46-, TX46+, TX46_4GND, TX47-, TX47+, TX47_4GND, TX48-, TX48+, TX48_4GND, TX49-, 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TITLE			
<i>M3</i> <i>DVI-I</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>X01</i>
SHEET		24	OF 49

CHANGE by		EMMA	23-Aug-2010	A3	CS	Model No	A01
				SHEET		24	OF 49

DVI-D



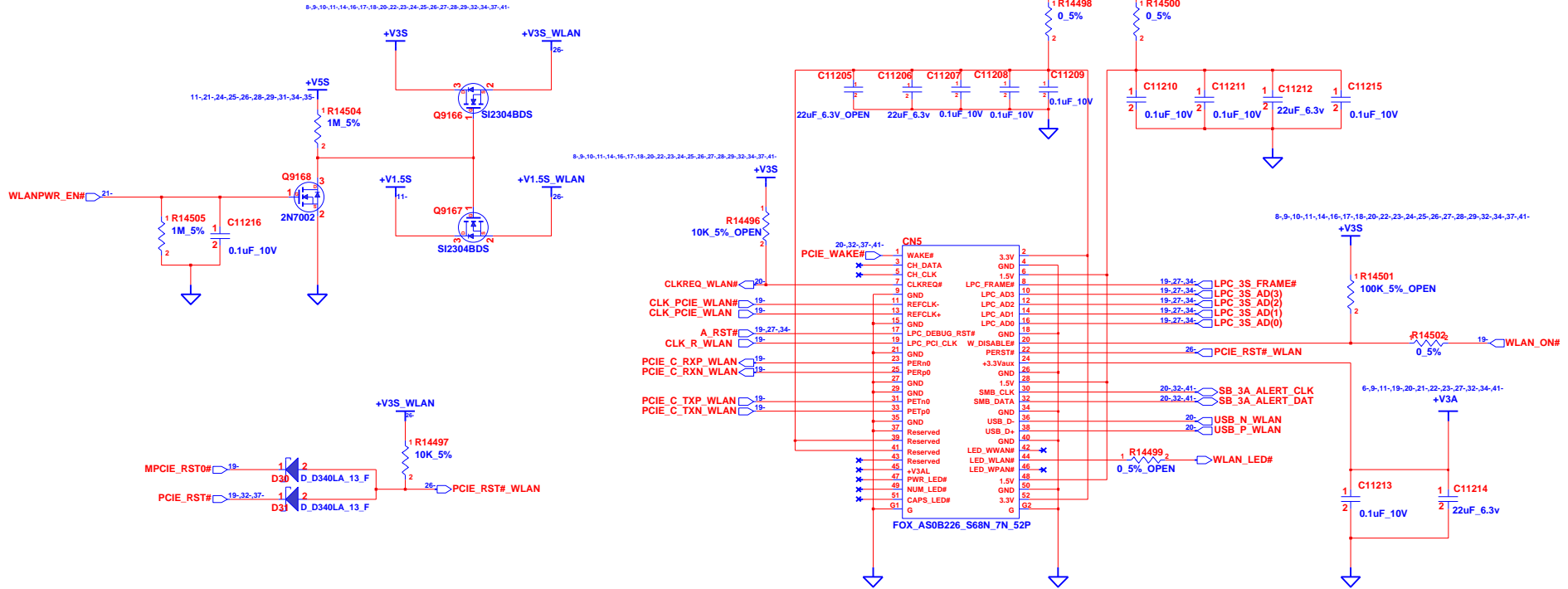
INVENTEC

TITLE			
M3			
DVI-D			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		25	OF 49

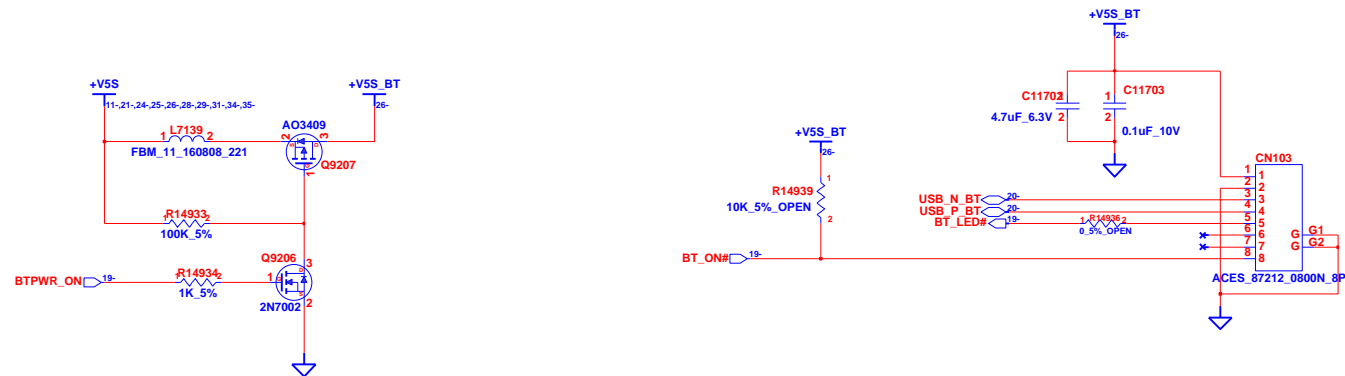
CHANGE by EMMA 23-Aug-2010

WLAN

MINI CARD CONN (WLAN)



BT

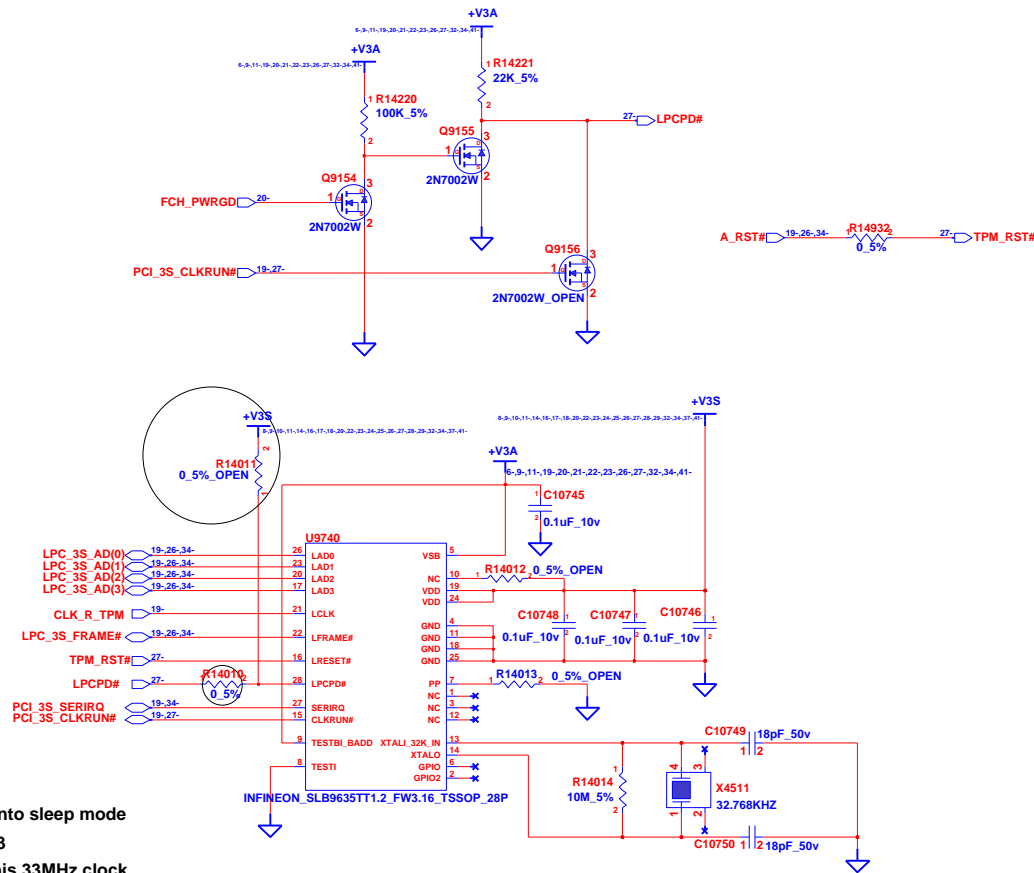


INVENTEC

TITLE			
M3			
WLAN/BT			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		26	OF 49

CHANGE by EMMA 23-Aug-2010

TPM



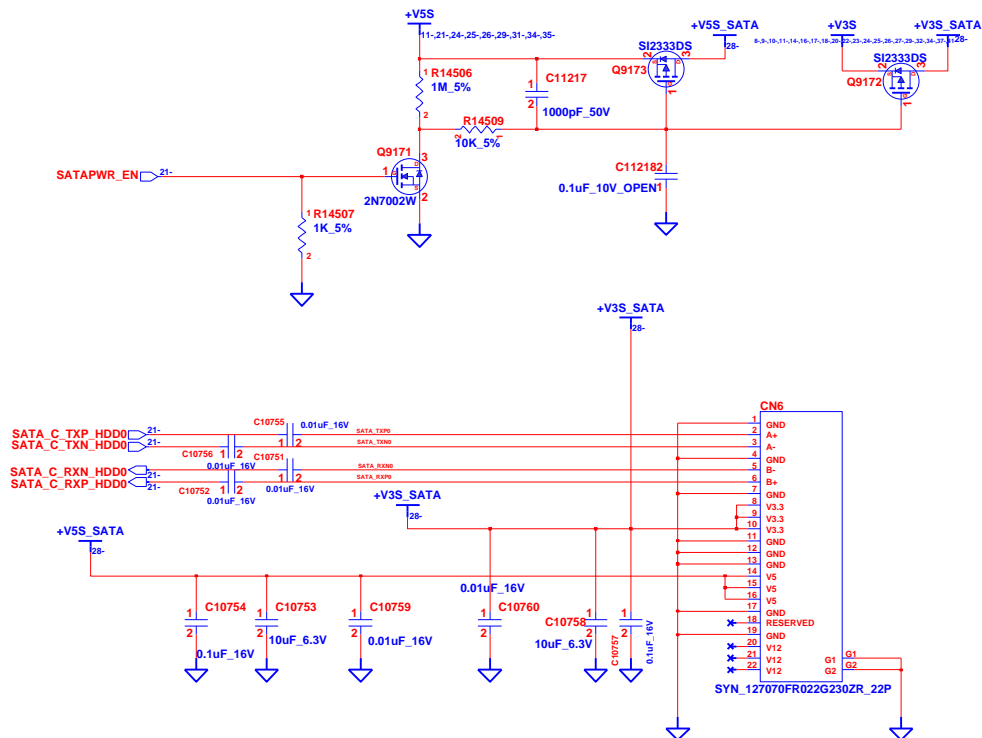
LPC_PWRDWN_L is used to force LPC peripherals to go into sleep mode
 Device may require that this be asserted before entering S3
 Device may require that this be asserted before stopping this 33MHz clock

INVENTEC

TITLE			
M3			
TPM			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
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SATA FLASH CONN.



INVENTEC

TITLE			
M3			
SATA FLASH CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

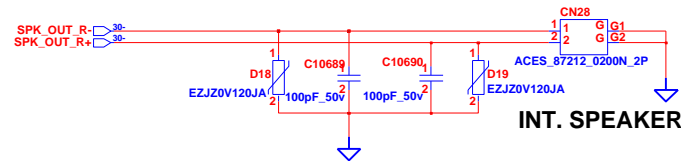
CHANGE by EMMA 23-Aug-2010

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[illegible]

TITLE <i>M3 CODEC</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>X01</i>

CHANGE by <i>EMMA</i>		23-Aug-2010	A3	CS	Model No	A01
			SHEET 29 OF 49			
6	7	8				

[illegible]

AMP

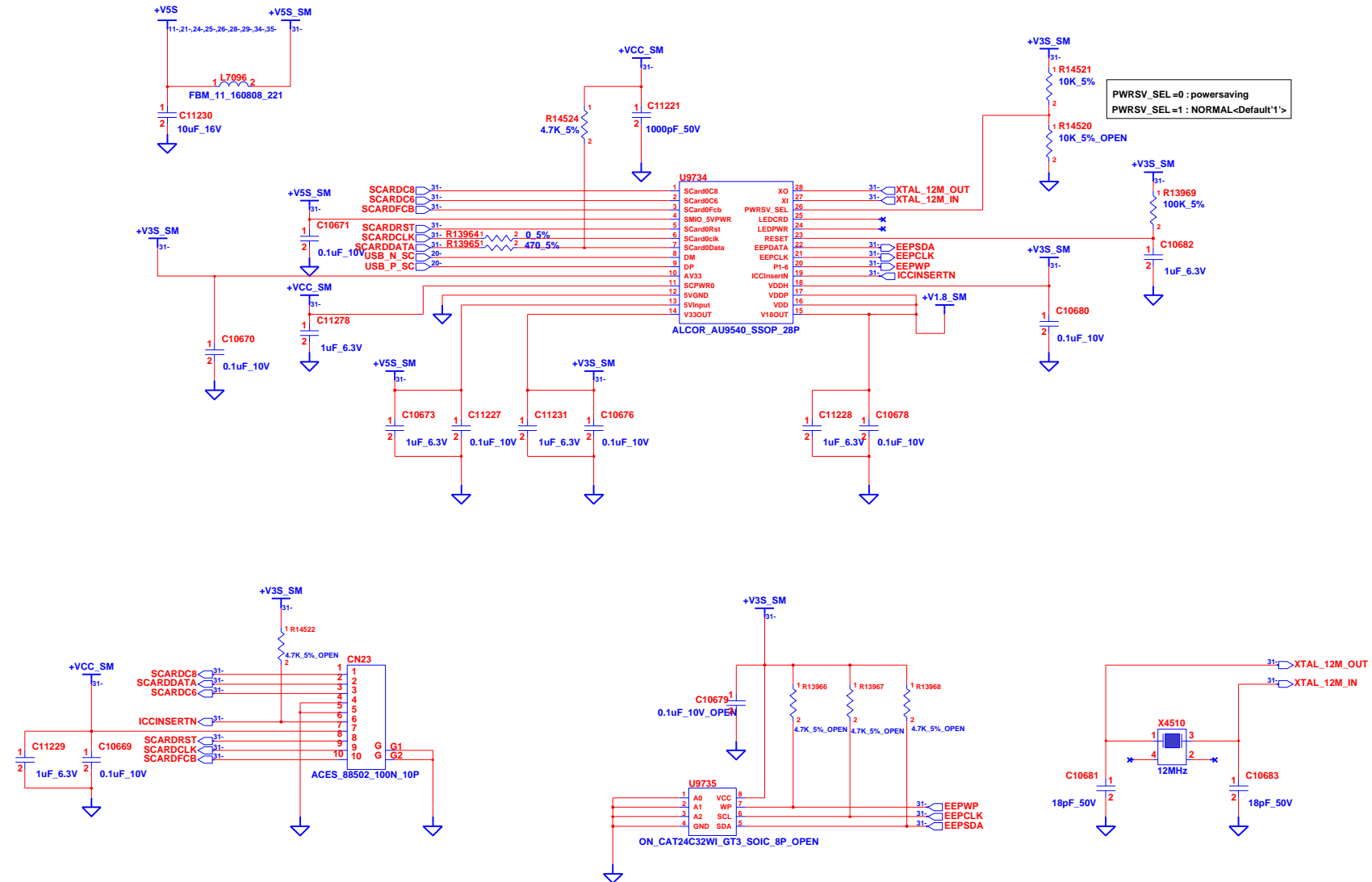
INT. SPEAKER

TITLE			
<i>M3</i> <i>AMP/SPEAKER</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>X01</i>
SHEET		30	OF 49

CHANGE by	EMMA	23-Aug-2010
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CS	Model No		
SHEET	30	OF	49

SMART CARD



INVENTEC

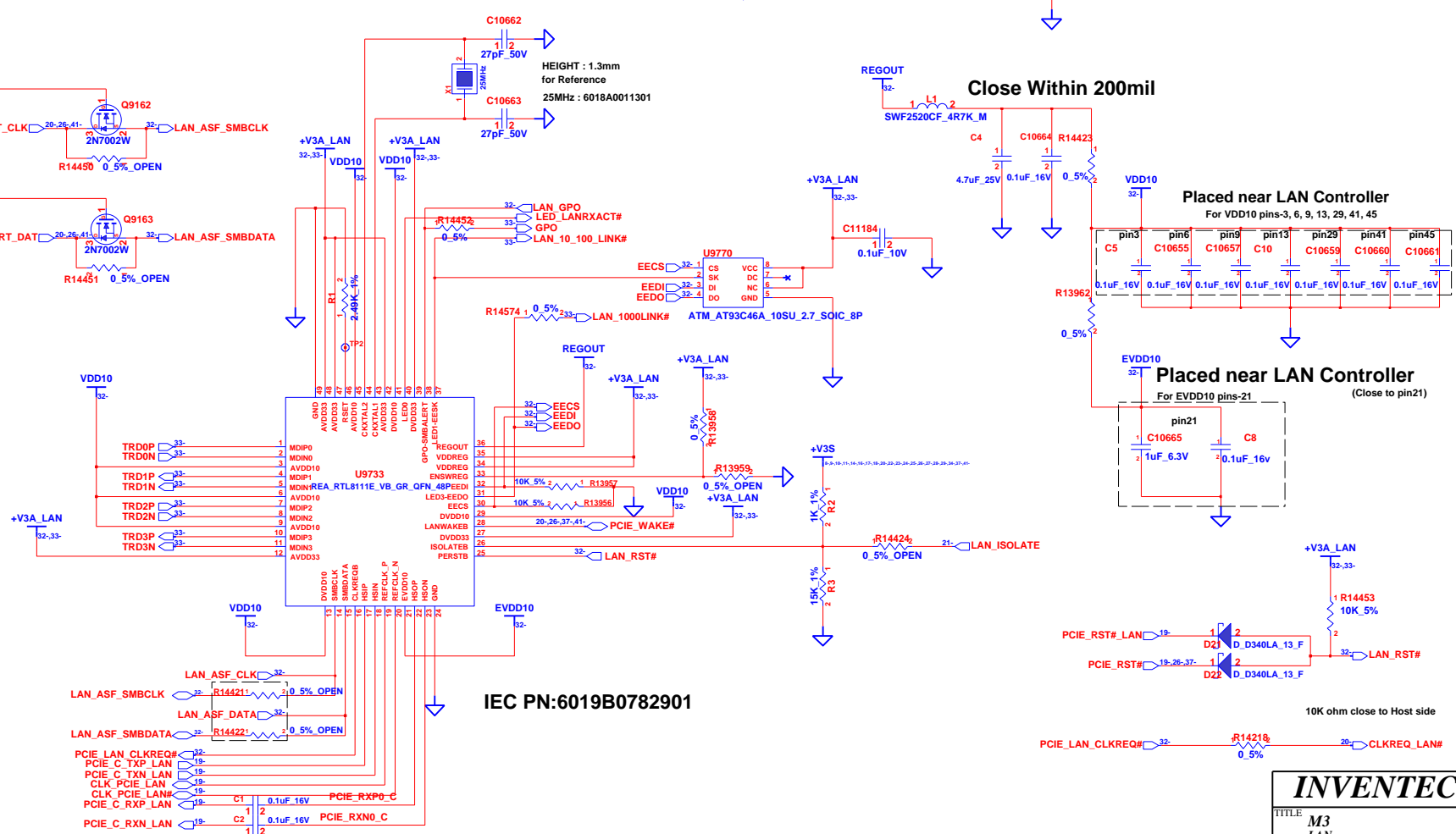
TITLE			
M3 SMART CARD AU9540			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		31	OF 49

CHANGE by	EMMA	23-Aug-2010
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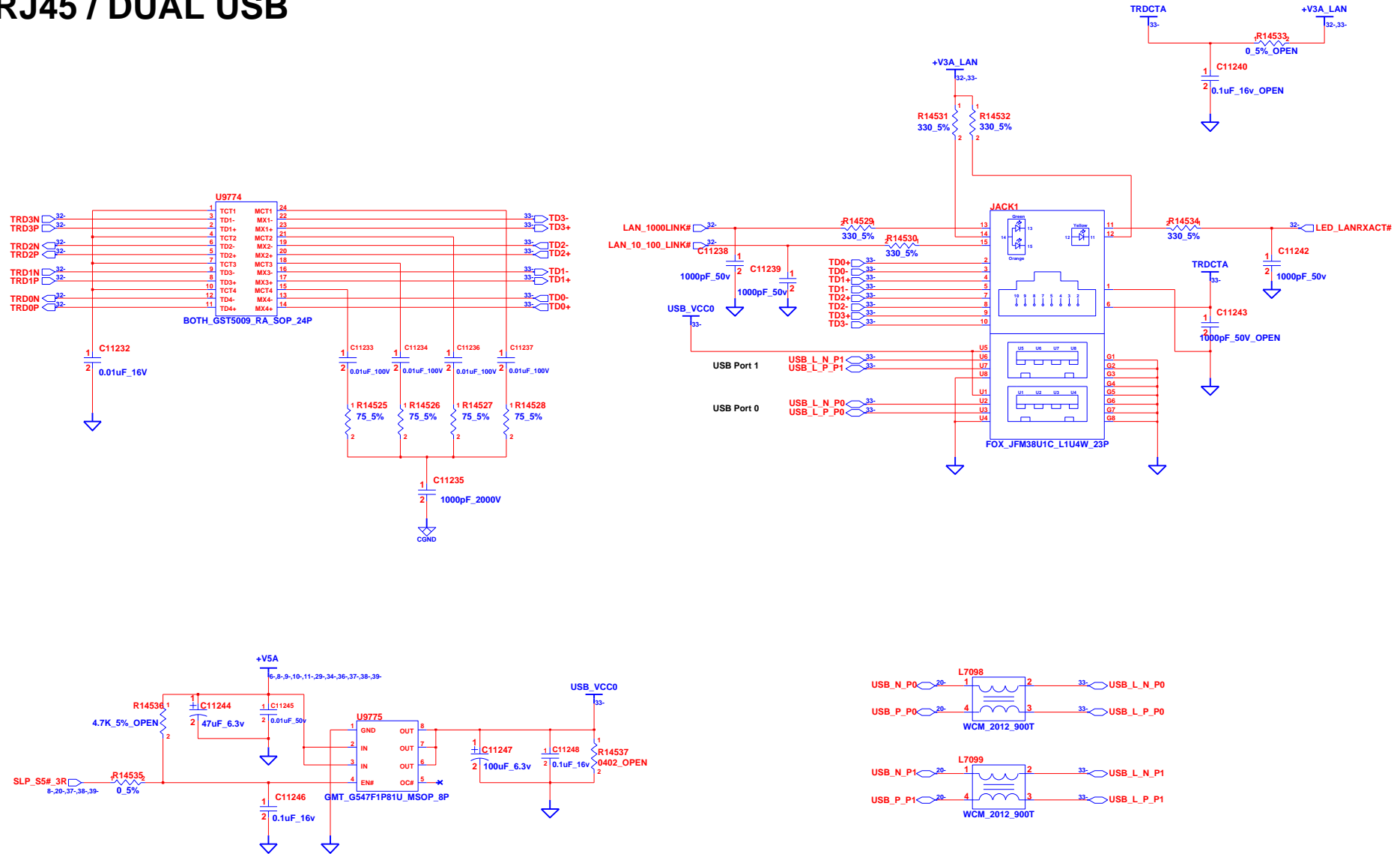
LAN

Defult using EEPROM only without ASF

	R14415	R14416	R14412	R14421	R14422
ASF	1K_5%	OPEN	1K_5%	0_5%	0_5%
without ASF	OPEN	10K_5%	OPEN	OPEN	OPEN



RJ45 / DUAL USB



INVENTEC

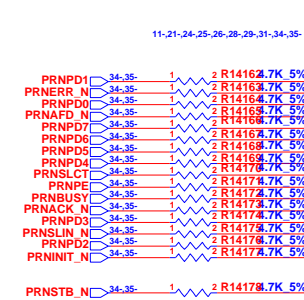
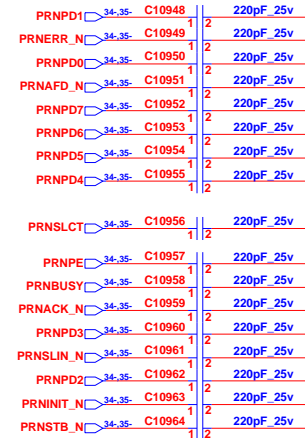
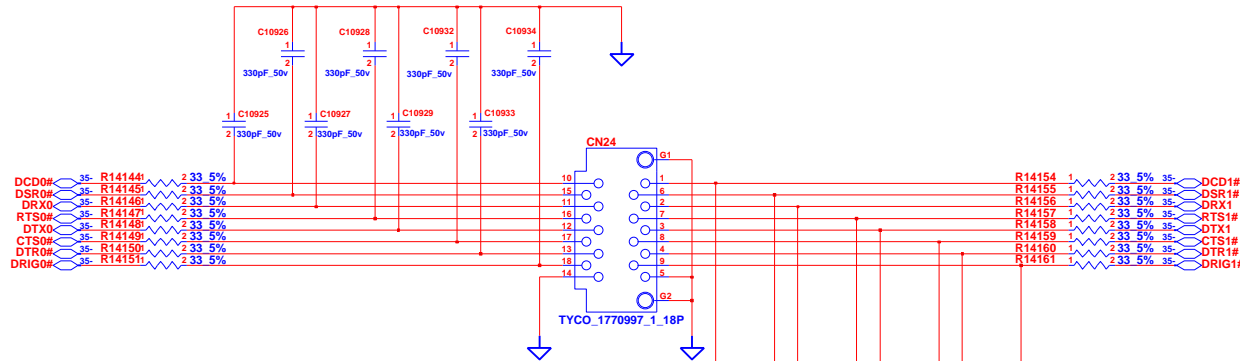
TITLE			
M3			
RJ45/ DUAL USB			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		33	OF 49

CHANGE by EMMA 23-Aug-2010

[illegible]

8

SERIAL/PARALLEL CONN.

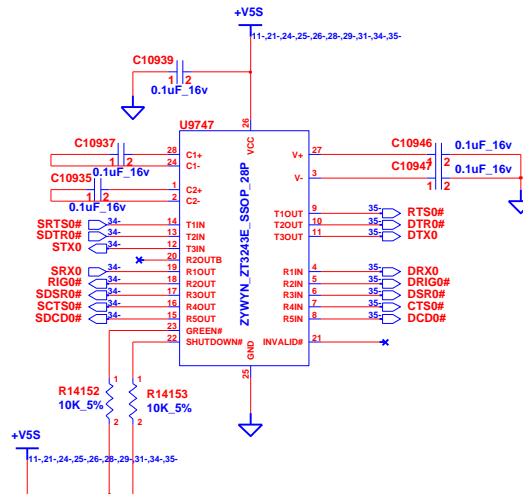
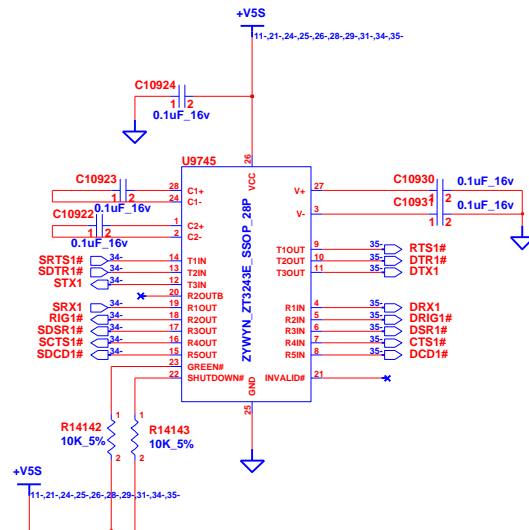
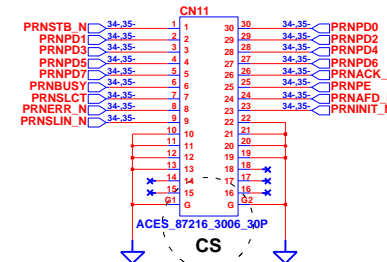


Note:

Cable Header

2	1	30
4	3	29
6	5	28
8	7	27
10	9	26
12	11	25
14	13	24
16	15	23
18	17	22
20	19	21
22	21	20
24	23	19
26	25	18
28	27	17
30	29	16

Parallel port Header

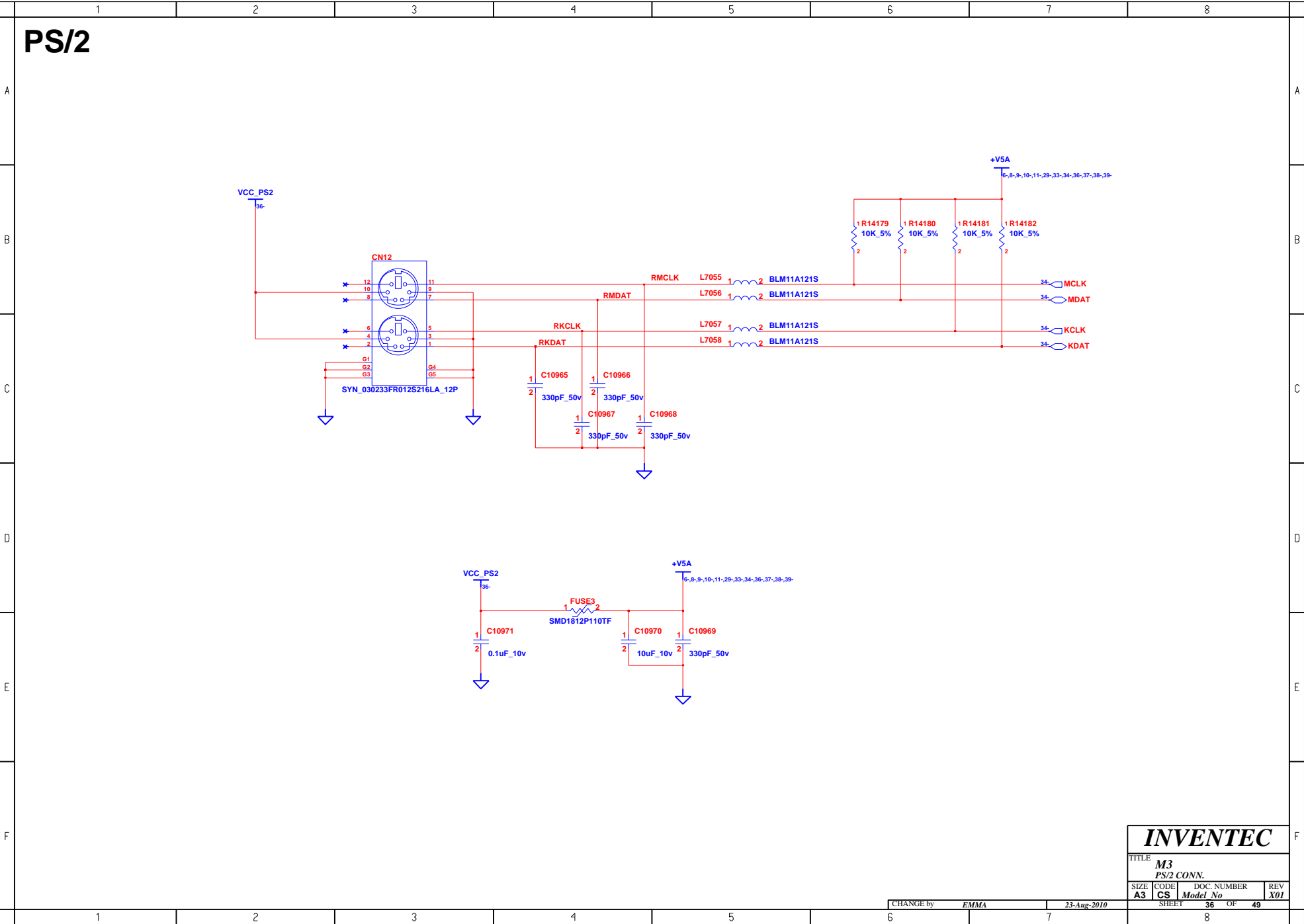


INVENTEC

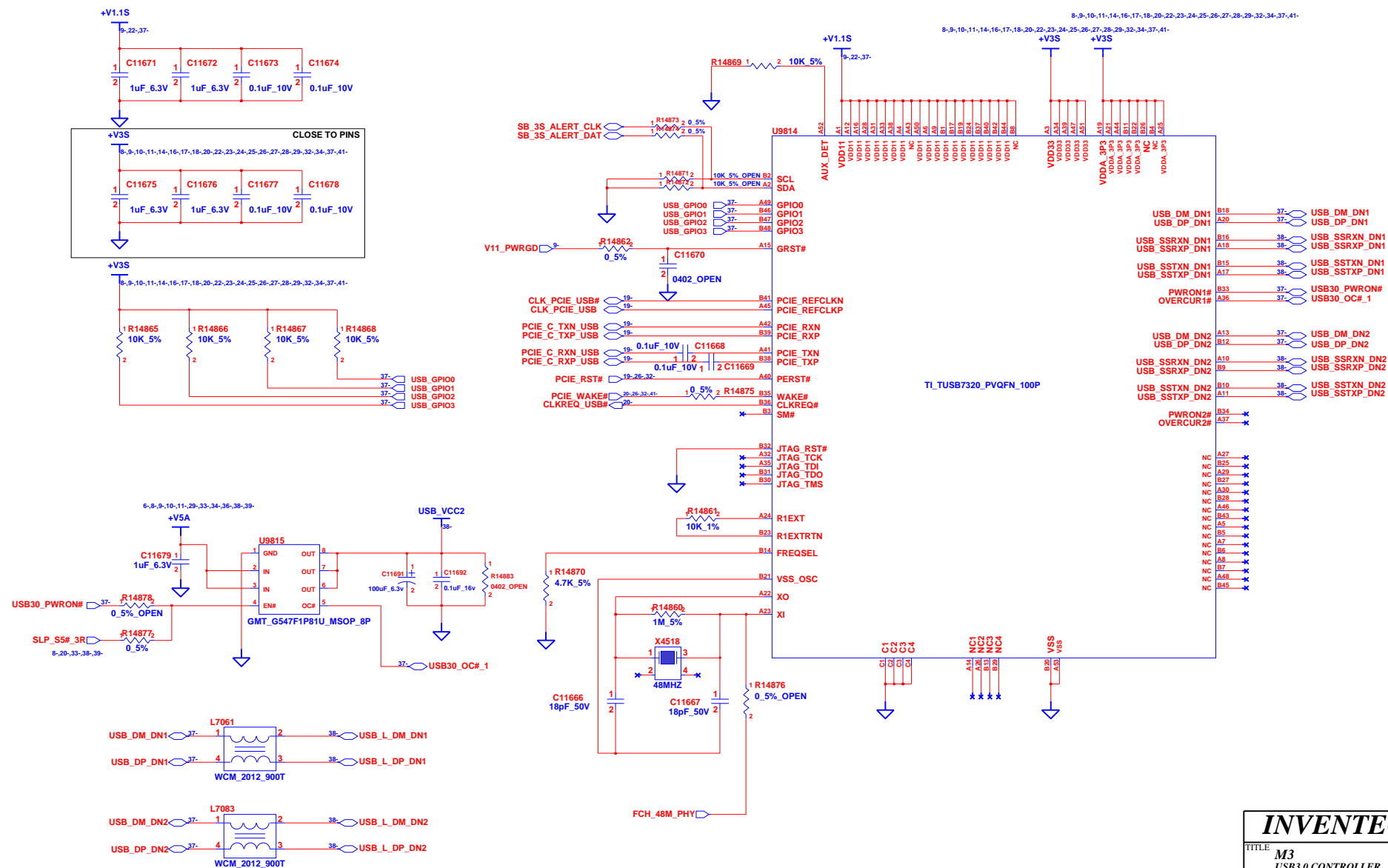
TITLE	M3
SERIAL/ PARALLEL CONN.	
SIZE	A3
CODE	CS
DOC. NUMBER	Model No
REV	X01
SHEET	35
OF	49

CHANGE by EMMA 23-Aug-2010

8

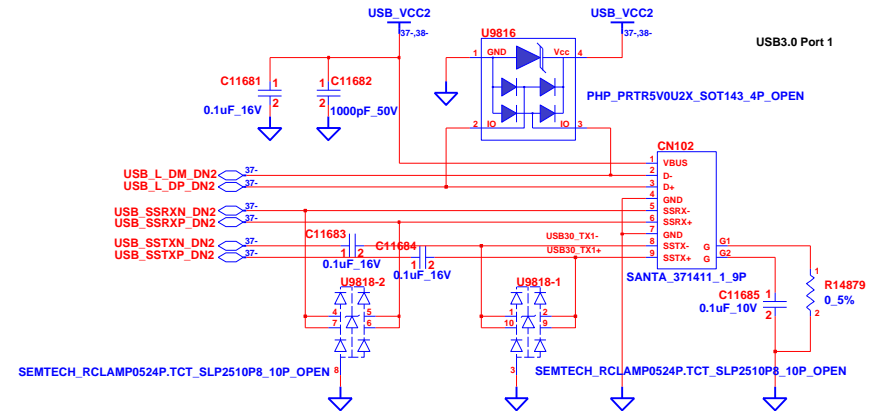
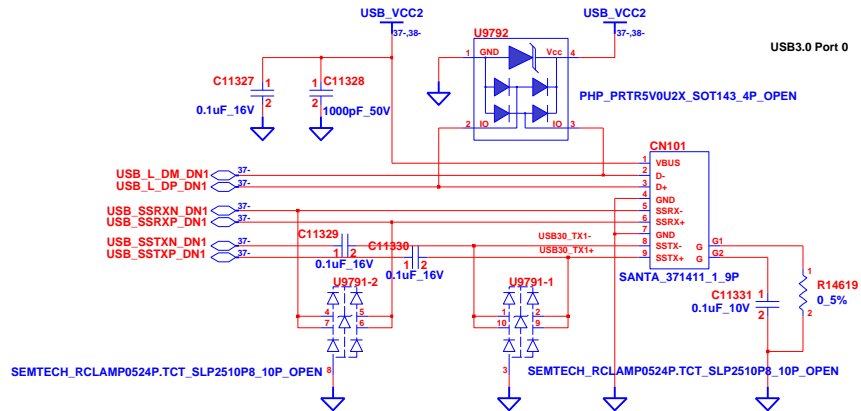
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USB3.0 CONTROLLER

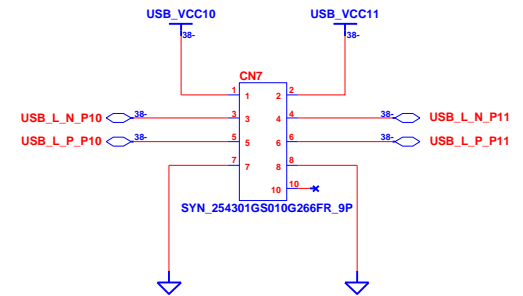
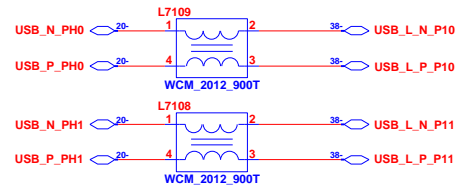
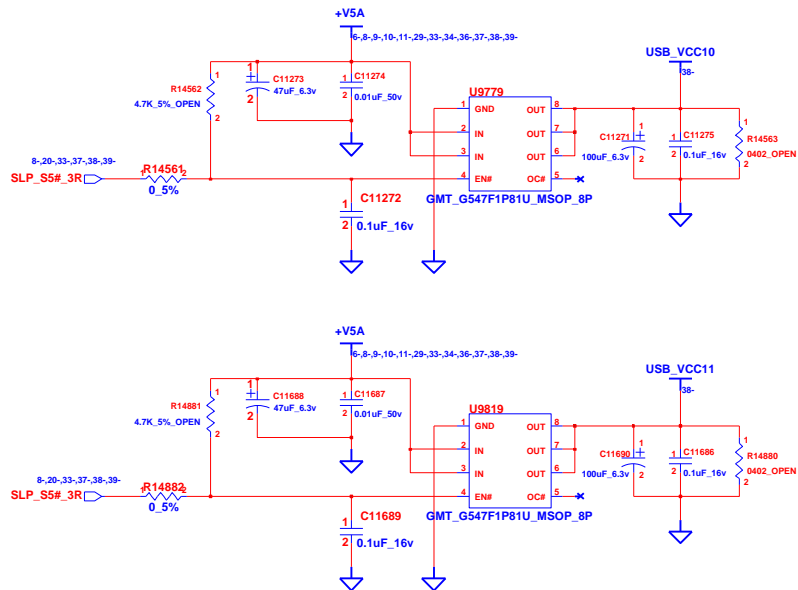
**INVENTEC**

TITLE			
M3 USB3.0 CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	X01

DUAL USB 3.0 CONN



USB HEADER



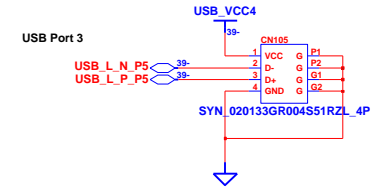
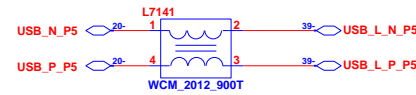
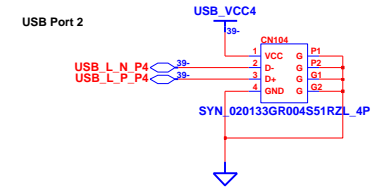
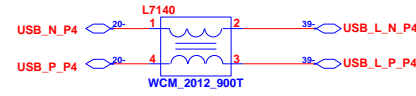
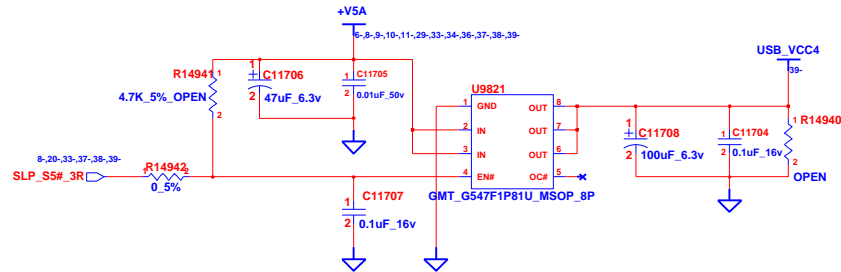
INVENTEC

TITLE			
M3			
DUAL USB3.0 CONN./USB HEADER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

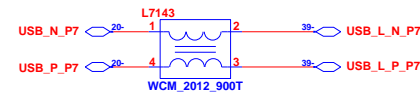
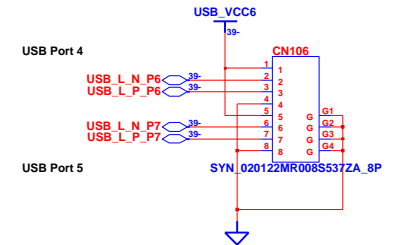
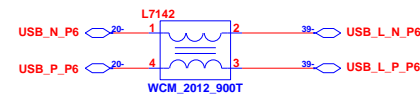
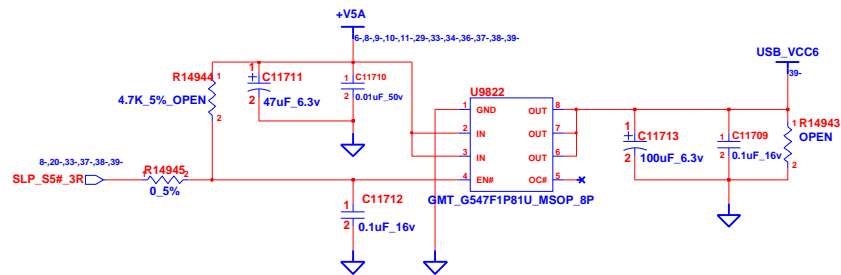
CHANGE by EMMA 23-Aug-2010

SHEET 38 OF 49

USB CONN



VERTICAL USB



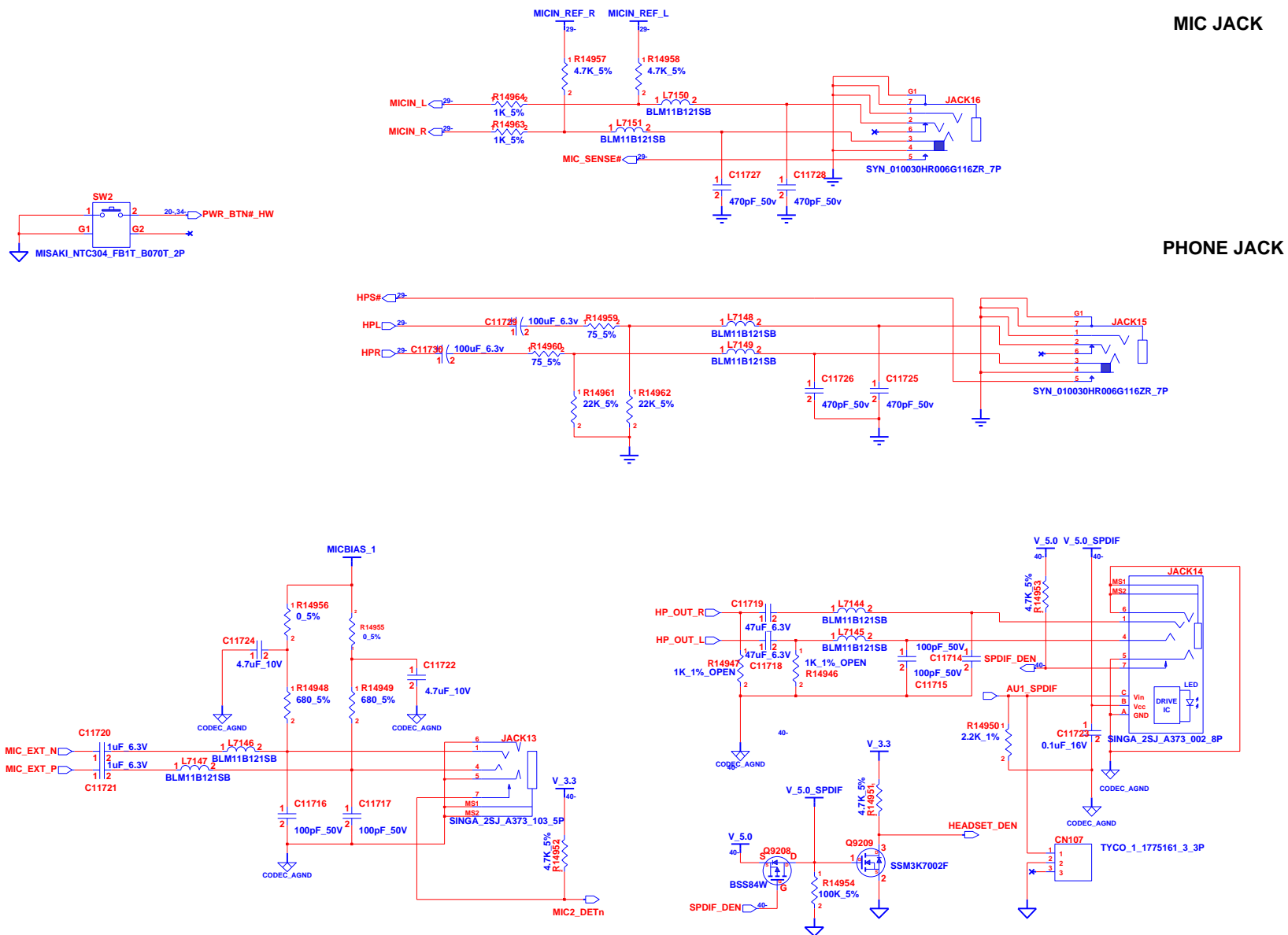
INVENTEC

TITLE			
M3			
RISERCARD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by EMMA 23-Aug-2010

SHEET 39 OF 49

AUDIO JACK / POWER SW



INVENTEC			
TITLE M3 B TO B CONN.(MB SIDE)			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
SHEET 40		OF 49	

RISERCARD CONN

Power and Signal Connections:

- +V3A:** 6-9,11-19,20-21-22-23-26-27-32-34-41- (via R14967, 4.7K 5% OPEN)
- +V3S:** 6-9,10-11-14-16-17,18-20-22-23-24-25-26-27-28-29-32-34-37-41- (via R14968, 4.7K 5% OPEN)
- +V12S:** 7-41- (via R14965, 0 5% and R14966, 20 5%)
- SB_3A_ALERT_CLK:** 20-26-32- (via R14965, 0 5%)
- SB_3A_ALERT_DAT:** 20-26-32- (via R14966, 20 5%)
- PCIE_WAKE#:** 20-26-32-37- (via R14965, 0 5%)
- PCIE_ARST#:** 19- (via R14966, 20 5%)
- PEG C TXP0, TXN0:** 14- (via R14965, 0 5%)
- PEG C TXP1, TXN1:** 14- (via R14966, 20 5%)
- PEG C TXP2, TXN2:** 14- (via R14965, 0 5%)
- PEG C TXP3, TXN3:** 14- (via R14966, 20 5%)

Pinout Table:

Pin	Signal	Pin	Signal
B1	+12V1	A1	PRSN1#
B2	+12V2	A2	+12V4
B3	RSVD1	A3	+12V5
B4	GND1	A4	GND6
B5	SMCLK	A5	JTAG2
B6	SMDAT	A6	JTAG3
B7	GND2	A7	JTAG4
B8	+3.3V1	A8	JTAG5
B9	JTAG1	A9	+3.3V2
B10	3.3VAUX	A10	+3.3V3
B11	WAKE#	A11	PWRGD
B12	RSVD2	A12	GND7
B13	GND3	A13	REFCLK
B14	HSOP_0	A14	REFCLK
B15	HSOP_0	A15	GND8
B16	GND4	A16	HSIP_0
B17	PRSN2#	A17	HSIN_0
B18	GND5	A18	GND9
B19	HSOP_1	A19	RSVD5
B20	HSOP_1	A20	GND16
B21	GND10	A21	HSIP_1
B22	HSOP_2	A22	HSIN_1
B23	HSOP_2	A23	GND17
B24	HSOP_2	A24	GND18
B25	GND12	A25	HSIP_2
B26	GND13	A26	HSIN_2
B27	HSOP_3	A27	GND19
B28	HSOP_3	A28	GND20
B29	GND14	A29	HSIP_3
B30	RSVD3	A30	HSIN_3
B31	PRSN2#1	A31	GND21
B32	GND15	A32	RSVD6

Capacitor Connections:

- +V3A:** C11740 (1uF_10v)
- +V12S:** C11731 (0.1uF_25v), C11732 (0.1uF_25v), C11733 (0.1uF_25v), C11734 (0.1uF_25v), C11741 (10uF_25v_K_X5R), C11742 (10uF_25v_K_X5R), C11735 (100uF_25v)
- +V3S:** C11736 (0.1uF_16v), C11737 (0.1uF_16v), C11738 (0.1uF_16v), C11739 (22uF_10v)

Connector: TYCO_1734774_7_64P

INVENTEC

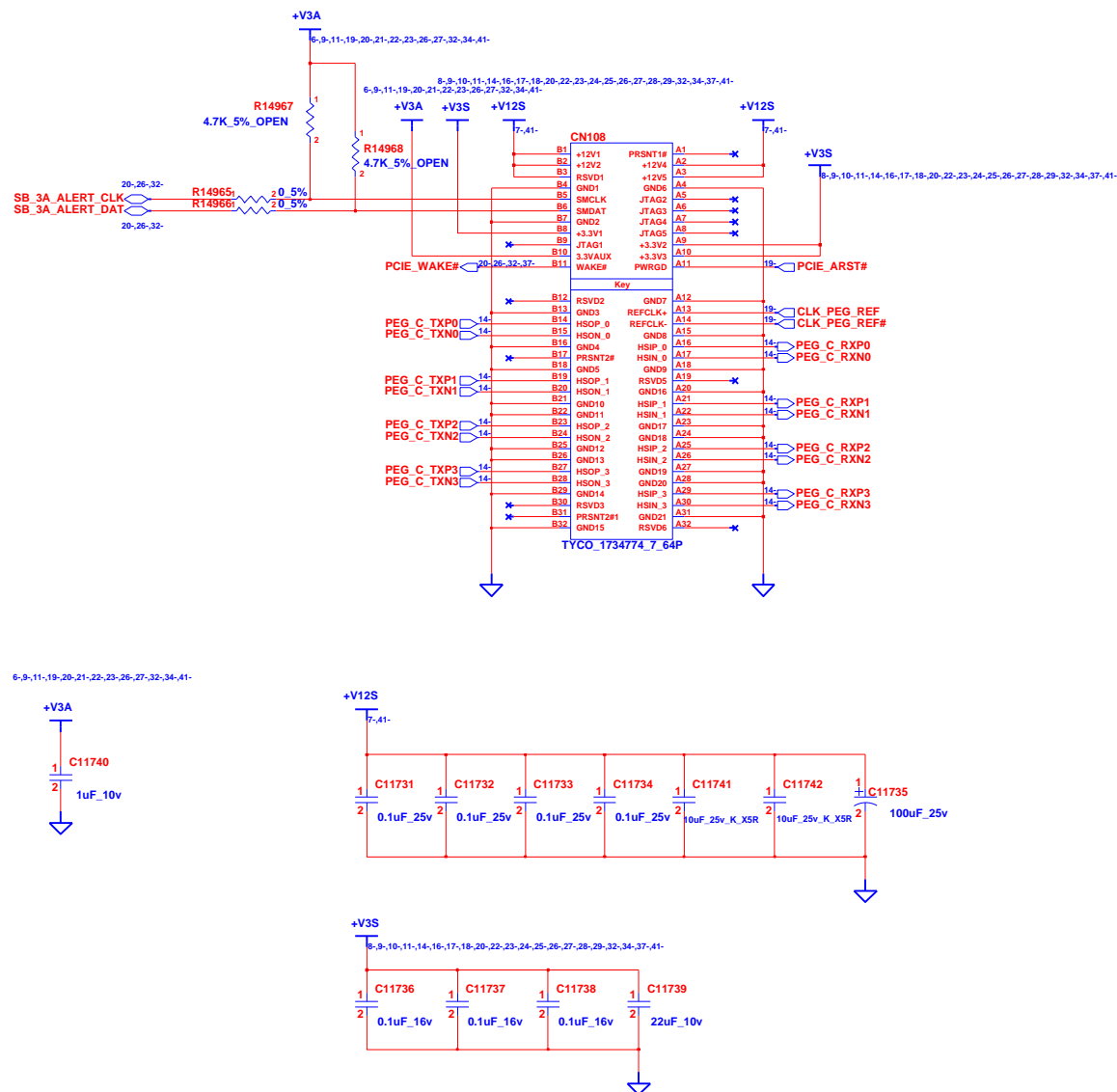
TITLE: M3
RISER CARD

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by: EMMA

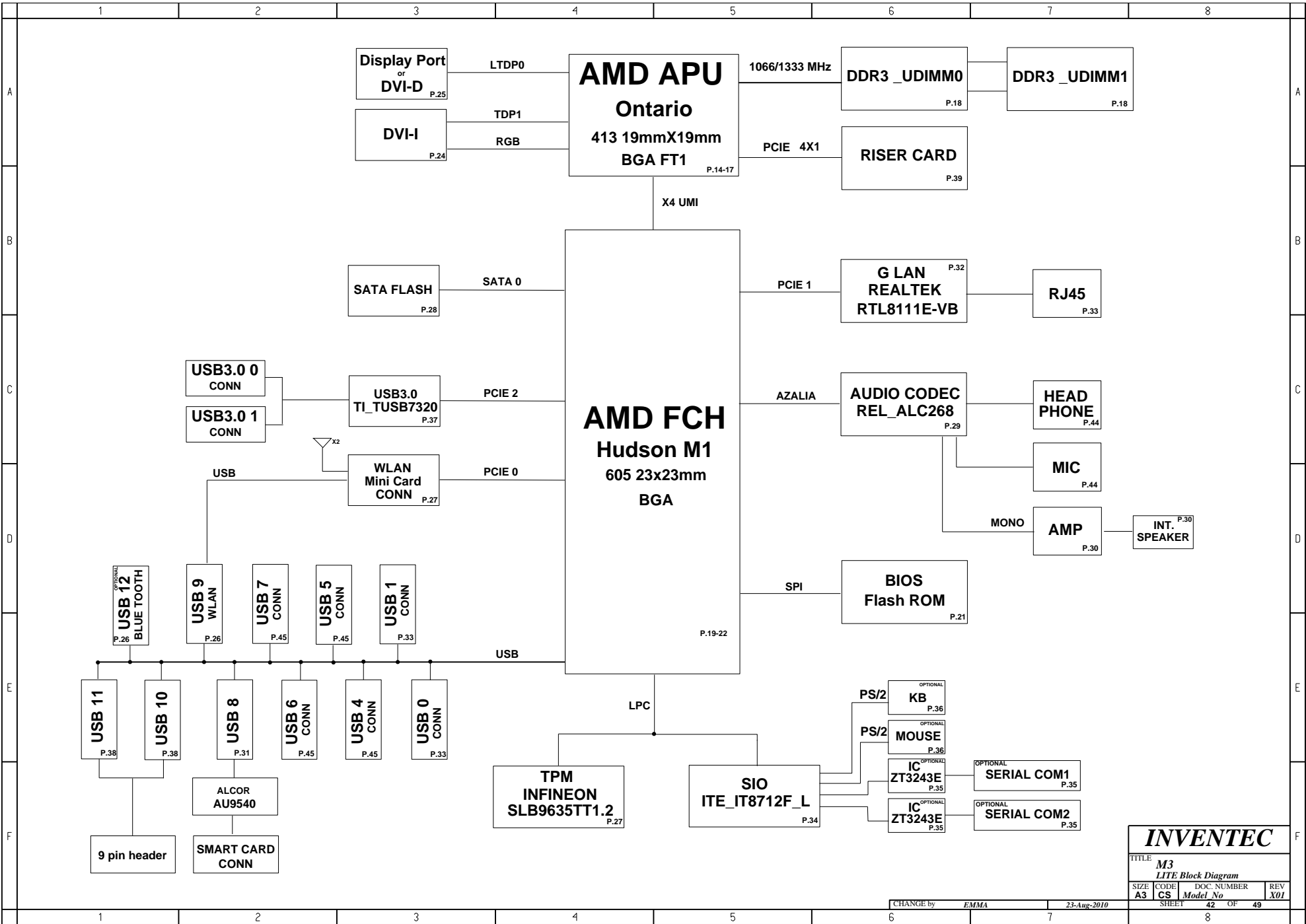
23-Aug-2010

SHEET 41 OF 49



TITLE			
<i>M3</i> <i>RISER CARD</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>X01</i>
SHEET		41	OF 49

CHANGE by	EMMA	23-Aug-2010
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	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>EMMA</i>		23-Aug-2010	
1	2	3	4	5	6	7	8		

INVENTEC			
TITLE <i>M3 DIAGRAM</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
SHEET		43	OF 49

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>EMMA</i>		23-Aug-2010	
1	2	3	4	5	6	7	8		

INVENTEC			
TITLE <i>M3</i> <i>AUDIO JACK/ PWR SW</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>X01</i>
SHEET		44	OF 49

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>EMMA</i>		<i>23-Aug-2010</i>	
	1	2	3	4	5	6	7	8	

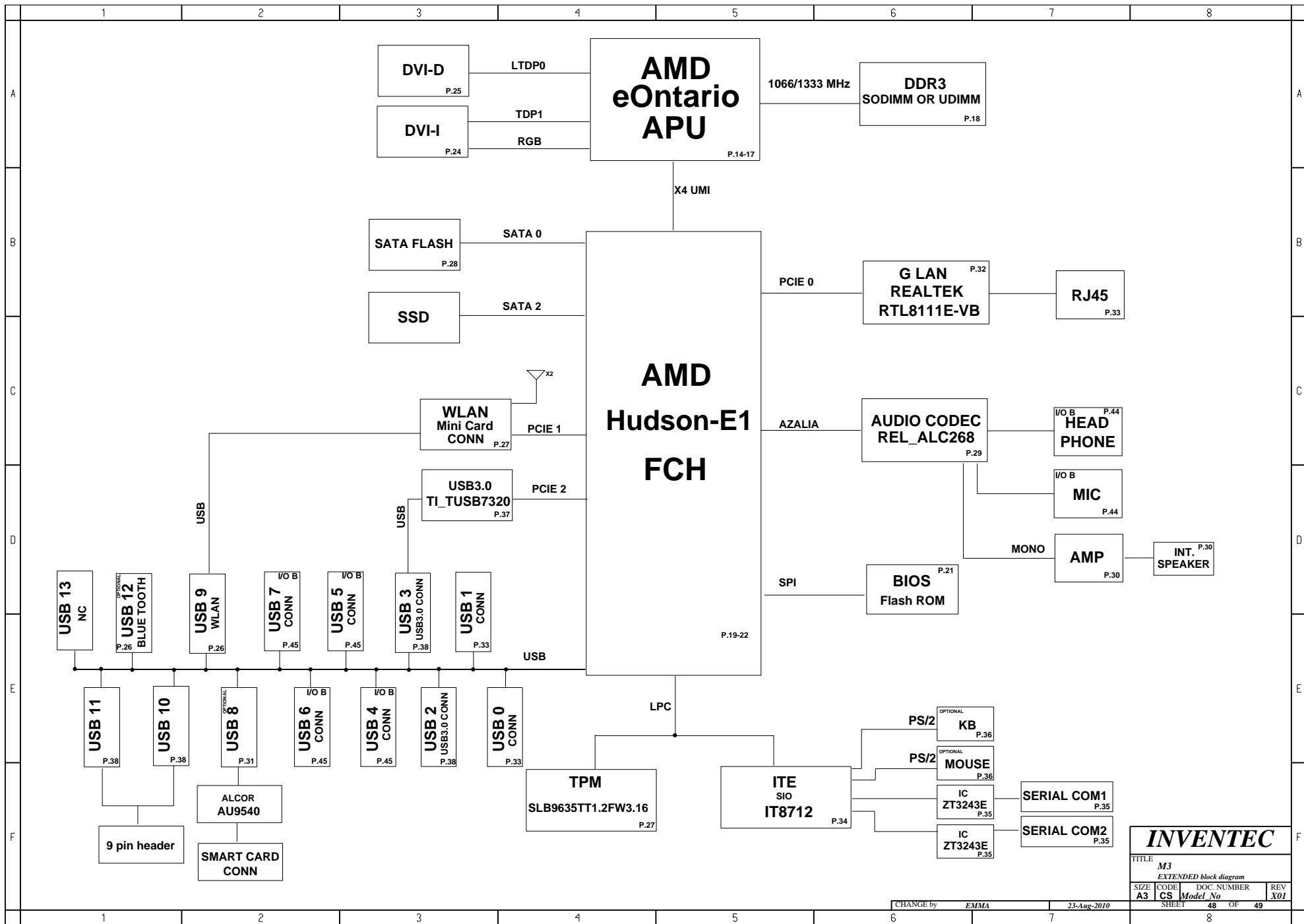
INVENTEC			
TITLE <i>M3 USB CONN.</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>X01</i>
SHEET		45	OF 49

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>EMMA</i>		23-Aug-2010	
1	2	3	4	5	6	7	8		

INVENTEC			
TITLE <i>M3</i> <i>RTSS138</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>X01</i>
SHEET		46	OF 49

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
						CHANGE by <i>EMMA</i>		23-Aug-2010	
1	2	3	4	5	6	7	8		

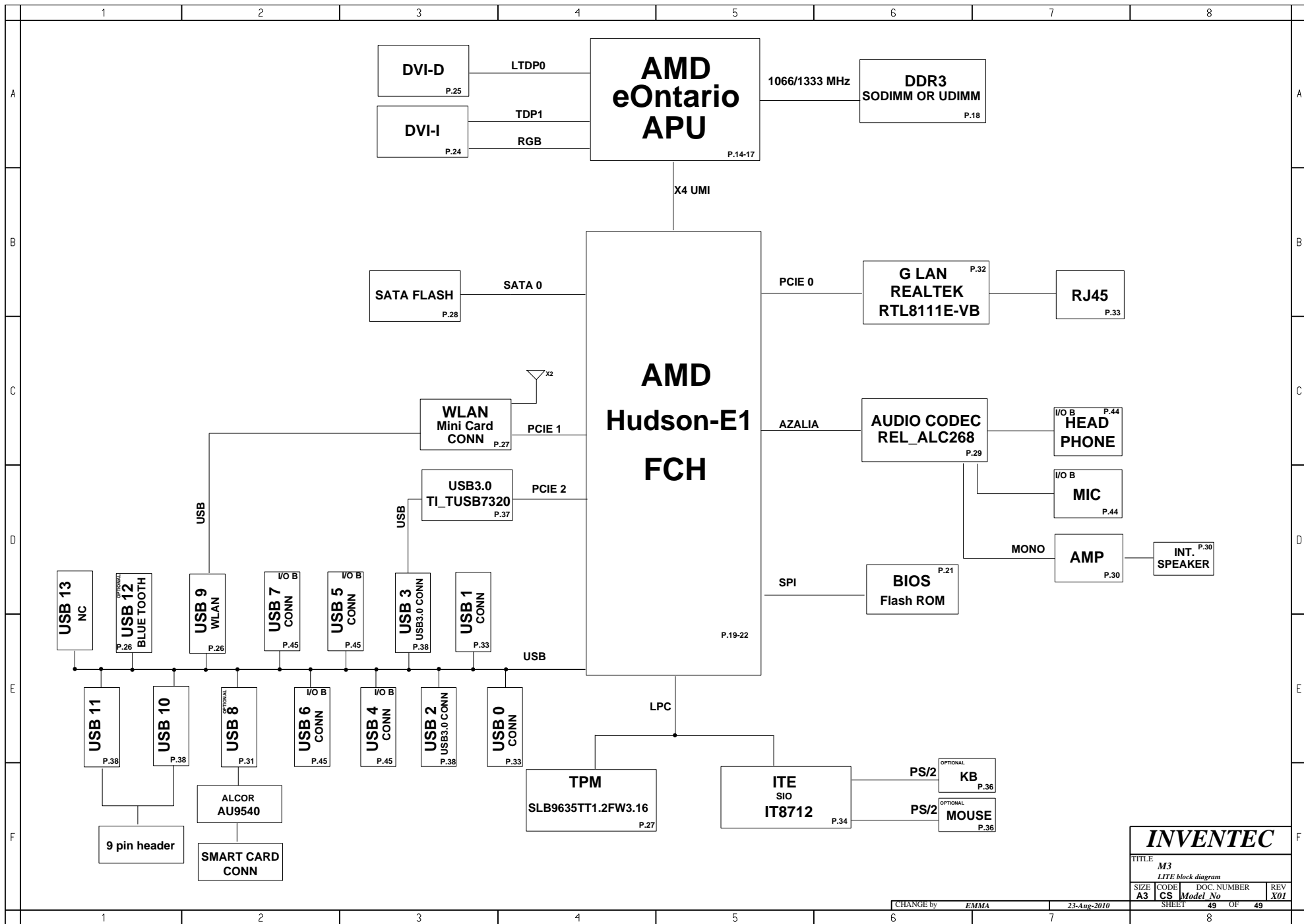
INVENTEC			
TITLE <i>M3 UDIMM</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>X01</i>
SHEET <i>47</i>		OF <i>49</i>	



INVENTEC

TITLE			
M3			
EXTENDED block diagram			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		48	OF 49

CHANGE by EMMA 23-Aug-2010



INVENTEC

TITLE			
M3			
LITE block diagram			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		49	OF 49